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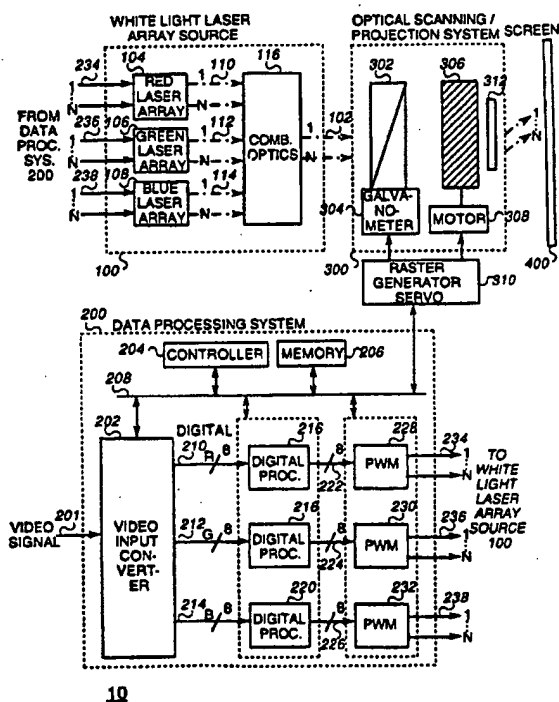
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(54) Title: METHODS AND APPARATUS FOR IMAGE PROJECTION

(57) Abstract

Method and apparatus (10) for a multi-application, laser-array-based image system are disclosed. The preferred embodiment of the invention utilizes three linear laser arrays (104, 106, 108). Each linear array generates multiple ($N > 1$) parallel output beamlets at one of the three primary colors (red, green, blue) (110, 112, 114). The corresponding 1 to N output beamlets of the three linear arrays, each individually modulated in luminance according to a specific encoding scheme representing the video image to be produced on the viewing screen (400), are combined spatially to form a single white light linear array source (100). Through a projection/scanner optical system, the N output beamlets of the white light source are simultaneously directed to, and swept horizontally across a distant viewing screen (400), resulting in a swath of N lines of a graphic video image. By producing M contiguous swaths vertically down the viewing screen, a full image of $M \times N$ lines is produced. The red, green, and blue linear laser arrays may consist of arrays of semiconductor laser diodes made of suitable semiconductor materials so as to directly emit radiation at wavelengths corresponding to red, green, and blue colors, respectively. Alternatively, the red, green and blue color arrays may be formed by arrays of semiconductor laser diodes emitting radiation at twice the desired red, green and blue wavelengths, whose radiation is coupled to arrays of second harmonic generation crystal elements. In the present invention, the simultaneous writing of $N > 1$ lines of graphic video information (instead of $N = 1$ as in conventional laser projection systems) enables significant performance improvements in the optical projection/image-forming subsystems, the video luminance and chrominance encoding schemes, and the video information processing electronics.



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METHODS AND APPARATUS FOR IMAGE PROJECTION

BACKGROUND OF THE INVENTION

This invention relates generally to a high resolution, image projection system employing at least one linear array laser sources, suitable for displaying raster scanned monochrome, two color and full color images such as television video or computer generated text and graphics by projection onto a screen.

Conventional cathode ray tube (CRT) display devices become impractical for large screen sizes. The present limit for graphic images is a maximum screen size of 45 inches square. The largest CRT consumer TV display generally does not exceed 40 inches measured diagonally across the screen.

For graphics image display, the ability of the CRT within a monitor to produce a proportional number of pixels for a unit screen area decreases as the tube size gets larger. This limits the ability of larger CRTs to produce the fine detail needed in graphics displays viewed at close distances. This occurs because of the difficulty in accurately reproducing the super fine red, green, and blue (RGB) phosphor trios or stripes over a larger face area without error during the manufacturing process. Furthermore, errors introduced in the deflection of the electron beam are magnified by the distances associated with the larger tube and cause spatial distortion of the pixel information on the screen.

In contrast to graphics displays, consumer TV displays must be bright and have high contrast. Since consumer TV displays are viewed at much greater distances than graphics displays, resolution is not an overriding factor. Additionally, CRTs generate

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low frequency electromagnetic fields and X-rays. Methods that are used to achieve high brightness cause the electron beam spot size to increase dramatically. As a consequence, this larger spot size causes a large reduction in image sharpness, that in turn reduces contrast and results in soft looking images. Additionally, larger TV screens require a larger deflection angle to prevent the CRT from becoming excessively long. Large deflection angles (over 90 degrees), coupled with large screen size, cause noticeable errors in linearity and color purity. Modern television CRTs were never intended to be large. The size, weight, and power trade-offs do not scale economically for large CRT systems.

As a result, video projectors using superposition of three CRT light sources for the primary colors of white light, namely red, green, blue (RGB), are commonly used. A standard product configuration uses three small diameter primary color CRTs and lenses to converge three separate colors at a screen image. Larger projection screen sizes, than those obtainable using a CRT, can be obtained using this method; however, the brightness and contrast are poor compared to that of a CRT used for home TV video viewing.

Another approach uses a single incandescent white light source to generate the primary colors that illuminate a LCD panel(s). The RGB pixels are independently modulated by the liquid crystal display ("LCD") selection matrices, that also generate the rastering. Although these projectors have fair resolution, there are other unavoidable problems related to this scheme. The incandescent white light source has a relatively short operating life and generates relatively large amounts of heat. The LCD

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5 devices cannot be manufactured without some minimum number of defects that, in turn, manifest themselves as permanent image artifacts on the screen regardless of the graphic or video source. Using LCD devices to generate the raster introduces a fixed and permanent resolution to the display device, making it very difficult to adapt the electronics to accept other resolutions for display of graphics and text information.

10 Brighter video projectors have been constructed using lasers. Typically, the green and blue beams are generated by argon ion gas lasers that directly emit green and blue radiation; the red beam is usually generated by a liquid dye laser (pumped with
15 part of the high power blue and green gas lasers). Generally, each of the three light beams is independently modulated to produce the same luminance and chrominance represented by an input video signal. The three modulated beams are then combined spatially
20 by optical means to produce a single so-called "white light" beam and directed toward the viewing screen by an appropriate raster/scanner optical system. Since only a single white light beam is projected toward the viewing screen in present gas-laser-based
25 projection systems, they are of the N=1 type, the number N specifying the number of white light beams. In such systems, generally, a full color picture (or frame) is produced at the viewing screen by projecting a series of pixels using a combination of
30 rotating and deflection mirrors. With proper synchronization, the rotating mirror scans the white light beam horizontally across the screen, sequentially painting a row of pixels; the deflection mirror simultaneously moves the white light beam
35 vertically down the screen, filling out the picture

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frame one line of pixels at a time. At any given instant, the white light beam illuminates a given pixel in the frame with the appropriate luminance and chrominance.

5 Present laser-based N=1 video projector systems are generally capable of producing a brighter image than non-laser based systems, and they can achieve close to 100% color saturation. They also exhibit pixel size stability, since the pixel size is
10 independent of white light beam power. In order to produce a reasonably bright image on a screen larger than 40 inches, the white light beam power at the screen should be in excess of one watt. Gas lasers used in present N=1 projection systems have power
15 efficiencies typically <0.1 percent; accordingly, present day gas-laser-based projection systems require several kilowatts of conditioned electrical power and conditioned cooling water sufficient to remove several kilowatts of waste heat. Such systems
20 are therefore relatively big, lack easy portability and are expensive. In present N=1 laser-based systems, three separate acousto-optic (AO) light modulators are used to impress video modulation information on each of the three RGB beams that forms
25 the single white light output beam. These modulators are problematic and costly. At the power levels needed for bright large-screen displays, modulation nonlinearities and other undesirable effects can degrade picture quality.

30 Associated with conventional laser-based video projectors has been the need to use high speed components, both mechanical for scanning and electronic for modulation in order to produce a standard television picture. Current NTSC television
35 pictures are reproduced at the rate of 1/30th of a

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second per frame, with each frame being filled by 525 horizontal scan lines. In a laser video projector, a multi-facet polygon mirror is typically used for scanning the single white light laser beam onto a screen. Even a 48-facet mirror would require an angular velocity in the region of 50,000 rpm. Bearings capable of such performance are very expensive. The scanning problem becomes even more critical when dealing with High Definition Television (HDTV) or high resolution graphics, since the pixel density increases thereby requiring an even higher angular velocity of the polygon mirror.

Various video standards, such as NTSC, and variations of HDTV and computer standards already exist, and new standards will in time be proposed. Among other considerations, these video standards may differ in resolution, picture aspect ratio, frame rate, and interlacing method. Therefore, it would be desirable to have an image projector capable of, or easily adapted to, displaying video pictures conforming to various existing or future video standards.

Thus, there is a tremendous need for a bright, low cost compact video and graphic image projector capable of displaying multiple resolutions of video pictures such as NTSC, HDTV, and high resolution graphics pictures. Furthermore, since the requirements for display of graphics and TV video are different, it would be very desirable to combine the display function of both types of images into a single portable projection unit.

SUMMARY OF THE INVENTION

Accordingly, it is an object of the present invention to provide an improved method and apparatus

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for video and graphic image projection, capable of reproducing various video standards such as NTSC, HDTV, computer graphics, and other high resolution video pictures.

5 It is another object of the present invention to provide a low cost method and apparatus for monochrome, two color and full color image projection that is amenable to mass production.

10 It is yet another object of the present invention to provide a method and apparatus for image projection that is power efficient and compact.

 It is a further object of the present invention to provide a laser white light source array that is amenable to mass production.

15 It is additional object of the present invention to provide a method and apparatus for image projection that uses an optical scanner with relatively slow rotational speeds.

20 It is another object of the present invention to provide a method and apparatus for image projection system that uses pulse width modulation to generate a high contrast video image.

 These and additional objects are accomplished by an improved image projection system comprising three
25 major subsystems: 1) a laser-based linear-array white light source; 2) a system for formatting an input video picture data suitable for digital two-dimensional processing; and 3) an optical scanning/projection system for directing multiple
30 beams of a white light linear array source onto a distant viewing screen.

 According to one aspect of the invention, a plurality ($N > 1$) of parallel output beamlets of a laser-based linear array single color, two color or
35 full color (also referred to herein as "white light")

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source are scanned horizontally across the screen by means of a rotating polygon mirror, illuminating the screen in a swath of N lines. An image resizing processor reformats input picture data into a form
5 suitable for driving RGB linear laser arrays that comprise the white light source. As part of the formatting process, the image resizing processor converts the input picture data into video frames. The optical scanning/projection system directs the N
10 parallel output beamlets from the white light array source onto the screen to recreate the input picture or image by filling the screen with illuminated pixels swath-by-swath, and frame-by-frame. The entire image projection system is controlled by a
15 system control microprocessor which receives input from a video input converter and from either a local control panel or an external computer.

According to another aspect of the invention, the laser-based white light source is produced by
20 spatially combining corresponding N parallel output beamlets of three primary color linear arrays, each of which provides N parallel output beamlets at the red, green, and blue wavelengths.

In one embodiment, the three primary color
25 linear arrays and combining optics, including dichroic mirrors, are mounted on a common base plate. The primary color red, green and blue beams are produced by several different methods, and the respective N output beamlets are modulated by several
30 different methods in several different information formats.

In another embodiment, each primary color linear array incorporates a linear array of N
35 individually-addressable semiconductor stripe laser diodes grown with a semiconductor material whose

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composition results in laser diode emission at the desired red, green and blue wavelengths. The light intensity of each of the N parallel-directed output beamlets is modulated by directly modulating the drive current supplied to each of the semiconductor stripe laser diodes. The N parallel-directed, modulated output beamlets of each primary color array are collimated by N corresponding micro-optic lenses contained in a linear lens array that is mounted monolithically with its respective primary color laser diode array.

In yet another embodiment, modulation is imposed on each of the N parallel-directed output beamlets of each of the primary color arrays by passing the beamlets through a corresponding optical coupling lens and a corresponding solid state electro-optic modulator. The coupling lenses and the solid state modulators are contained in linear arrays mounted monolithically with the semiconductor laser diode array and output collimating lens array.

In another embodiment, each primary color linear array consists of a linear array of N individually-addressable semiconductor stripe laser diodes emitting parallel beamlets at an infrared wavelength equal to twice the desired red, green, or blue wavelength. The infrared output of each such array is converted to an array of red, green, or blue output beamlets by passing each beamlet through a corresponding optical coupling lens and stripe waveguide made of an appropriate nonlinear crystalline material. The N coupling lenses and nonlinear waveguides are contained in a linear lens array and waveguide array, respectively, mounted monolithically with the semiconductor laser diode array and output collimating lens array. Image

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information modulates the N parallel-directed output beamlets of each primary color array, either by directly modulating the drive current of the individual semiconductor stripe laser diodes in the array, or by incorporating an array of solid state electro-optical modulators following the nonlinear waveguide array and prior to the output collimating lens array. In the alternative, a red beamlet may be directly generated by use of a suitable semiconductor material without the need for a nonlinear crystalline material harmonic generator.

According to another aspect of the invention, the video information impressed on the N beamlets of each of the three primary color arrays may take several different forms.

In one embodiment, the modulation is implemented by modulating the drive current of the individually addressable semiconductor stripe laser diodes. This differs from acousto-optic modulation utilized in conventional (N=1) laser projector systems because semiconductor laser diodes can be abruptly turned on and off at high rates. In the present invention, pulse width modulation ("PWM") is used without the requirement of prohibitive high-speed/high bandwidth electronics since the simultaneous scanning of N lines of the video image greatly increases the time available to scan a given swath across the screen at a given frame rate. Since N lines are processed in parallel, the data processing rate is reduced by the factor N. For example, a PWM method for $2^7 = 128$ grey levels may require an increase of data processing rate by a factor of 128. This increase can be offset if N=128 lines are scanned in parallel, since the time to process each pixel is then reduced by a corresponding factor of N=128.

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According to another aspect of the invention, the optical scanning/projection system comprises a rotating polygon mirror with progressively tilted facets (hereinafter also referred to as an "irregular polygon mirror") to scan the white light array output horizontally across the screen to also raster the swath scan vertically down the screen. The rotating polygon has at least as many facets, M , as the number of swaths required to fill the picture frame. Starting from a first facet, a first swath is "painted" across the top of the picture frame. Each successive facet is progressively tilted so as to paint each successive swath directly below the preceding one and contiguous with it, until the picture frame is filled, whereupon the first facet once again begins to paint a swath at the top of the next frame. Since a complete frame is painted using a tilted facet polygon mirror, no galvanometer is required to raster each successive swath vertically down the screen.

In another embodiment, the rotating polygon scan mirror has M facets whose normals maintain a constant angle relative to the mirror spin axis (hereinafter also referred to as a "regular polygon mirror"). To raster each successive swath vertically down the screen, a galvanometer is placed between the rotating polygon scan mirror and the laser array.

An important feature of the present invention is the simultaneous parallel scanning of N pixel lines. As mentioned in connection with PWM above, this parallel processing feature scales down the required speed of associated electronics. A similar mechanical advantage is also gained for the optical scanning/projection system.

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In the present invention, each polygon mirror facet is employed to scan a swath of N lines instead of a single line. For a given fixed frame time, the angular velocity (also referred to herein as

5 "rotational speed") of the swath scan polygon mirror is reduced by a factor of N compared to that required for a single line scan polygon mirror. For example, if a swath of 128 lines is scanned, a 10-facet polygon mirror is required to scan and raster a

10 picture frame of 1280 horizontal lines. By comparison, if only one line is scanned at a time ($N=1$, as in conventional laser projectors) a 10 facet polygon mirror turning at an angular velocity 128 times higher will be required.

15 In one embodiment, an interlace galvanometer is optionally employed in tandem with the rotating polygon mirror having progressively angled facets (an irregular polygon mirror). This rotating polygon mirror by itself will only produce a progressive

20 (swath sequential) scan for each picture frame. In order to support interlaced video standards, an additional deflection offset is required on alternating fields and is performed by the interlace galvanometer. The interlace galvanometer performs a

25 minor shift to offset successive fields. For example, in a 2:1 interlace, the shift amounts to half a horizontal line spacing for the entire stream. A picture frame is then formed by interlacing two partial frames of "fields", where a field rate is

30 twice that of the frame rate.

In yet another embodiment, an interlace galvanometer is used together with the combination of a regular polygon mirror and a galvanometer which rasters each successive swath vertically down the

35 screen.

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According to another aspect of the invention, by increasing the number of N parallel-directed output beamlets to the desired number of raster lines, the rotating polygon mirror and interlace galvanometer can be eliminated, thereby reducing noise vibration, heat and projection errors attributable to the rotating mirror, its associated motor and driving electronics. In this embodiment a single mirror having a height at least equal to the height of N lines is used to provide horizontal deflection. If desired, interlacing may be provided with a separate interlace galvanometer.

According to another aspect of the invention, a data processing system comprises an image resizing processor and a frame storage unit. The image resizing processor transforms input picture data from various types of video standards to a format suitable for the image projection system of the invention. Essentially, the image projection system is designed with a preselected resolution. The image resizing processor maps a picture of a given video standard into the preselected resolution of the image projection system (e.g., represents the picture by the constant number of pixels of the laser projection system). The image resizing processor maps a picture from a lower resolution video standard to that of a high resolution video standard.

The electronic remapping of pictures from various video standards into a preselected resolution system is an important feature of the present invention. Such remapping is implemented with a hardware accelerator using interpolation. Digital methods are superior to conventional optical methods, and greatly simplify the electronics of the image projection system for displaying pictures from the

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various video standards having different resolutions, aspect ratios, and frame rates.

For example, the preferred embodiment of the present invention has a resolution defined by 1280 x 1280 pixels within a frame having a screen aspect ratio of 16:9. Pictures from many video standards can be displayed at that resolution. Thus, a HDTV picture will have the same screen aspect ratio, and the image resizing module will essentially map the digitized input video source picture into the 1280 x 1280 pixels of the laser projection system.

When an NTSC picture is remapped, since the picture has a screen aspect ratio of 4:3 (12/9), the active display picture will fall within a central band flanked by left and right margins. The pixels in the left and right margins (each occupying one-sixth of the horizontal dimension) will not be utilized. Thus, the picture is mapped by the image resizing processor onto the pixels (960 x 1280) in the central band.

Video standards with different vertical scan rates or frame rates are easily accommodated by varying the angular velocity (rotational speed) of the polygon mirror. Accordingly, increasing the angular velocity of the polygon mirror speeds up the field and frame rates.

The invention provides an image projection system that is power efficient, has high performance, is amenable to low-unit-cost mass-manufacturing processes, and is compatible with multiple video standards.

Additional objects, features, and advantages of the present invention will be understood from the following description of the preferred embodiments, a

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description which should be referenced in conjunction with the accompanying drawing.

BRIEF DESCRIPTION OF THE DRAWING

5 Figure 1 is a block diagram illustrating the color image projection system of the present invention;

 Figure 2A illustrates the line-by-line raster scan of a conventional single beam ($N=1$) imaging system, such as that of a CRT;

10 Figure 2B illustrates the swath-by-swath raster scan of the color image projection system of the present invention;

 Figure 3A is a perspective view illustrating the use of a polygon mirror having progressively tilted adjacent facets to project a first swath of N lines.

15 Figure 3B is a perspective view illustrating the use of a polygon mirror having progressively tilted adjacent facets to project a second swath of N lines.

 Figure 4 is a block diagram of a first alternative embodiment of the optical scanning/projection system of Figure 1;

20 Figure 5 is a block diagram of a second alternative embodiment of the optical scanning/projection system of Figure 1;

25 Figure 6A is a perspective view of a primary-color linear laser array source of the present invention;

 Figure 6B is an enlarged view of the area of Figure 6A indicated by line 6B-6B.

30 Figure 6C is an enlarged view of the area of Figure 6A indicated by the line 6C-6C.

 Figure 7A illustrates a top plan view of an embodiment of the white light laser array source of Figure 1;

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Figure 7B is a front plan view of the white light laser array source of Figure 7A.

5 Figure 7C is an enlarged view of the area of Figure 7B indicated by the line 7C-7C illustrating a linear array of integrated electro-optic modulators.

Figures 8A through 8D are a diagram of an image projection system of the present invention detailing the data processing system of Figure 1.

10 Figure 9 is a diagram that illustrates a picture frame being mapped by the image resizing module to another picture frame with the same aspect ratio but with a higher resolution;

Figure 10A specifies parameters for the display of the HDTV: SMPTE 240m (1125) standard.

15 Figure 10B specifies parameters for the display of the proposed HDTV: 1050 NBC standard.

Figure 10C specifies parameters for the display of the NTSC: RS-170A standard.

20 Figure 10D specifies parameters for the display of the VGA (Monochrome/Color) standard.

Figure 11 is a timing diagram that illustrates a pulse-width-modulation method according to one embodiment of the present invention.

25 Figure 12 is a perspective schematic layout that illustrates in further detail one embodiment of the optical scanning/projection system of Figure 1.

Figure 13 is a perspective schematic layout that illustrates in further detail a second embodiment of the optical scanning projection system of Figure 1.

30 DETAILED DESCRIPTION OF THE INVENTION

Referring to Figure 1 a color image projection system 10 is shown. The color image projection system 10 includes a white light linear array source 100, a data processing system 200 for formatting

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input picture or image data into a suitable form to modulate the white light linear laser array source 100, and an optical scanning/projection system 300 for scanning and projecting the output beams of the white light linear laser array source 100 onto a projection screen 400. The output beams of the white light linear laser array source 100 may also be projected onto or into other photoreceptors such as an eye, paper, photosensitive film or other photosensitive materials.

The white light linear laser array source 100 produces a plurality N of white light parallel-traveling beamlets 102. Each of the separate beamlets is formed by spatially combining the N parallel-traveling output beamlets of each of three primary color linear array sources, a red linear array 104, a green linear array 106, and a blue linear array 108. Each of the parallel-traveling linear array outputs of single color N beamlets 110, 112 and 114 of the three primary color linear array sources 104, 106 and 108, respectively, is then combined by combining optics 116, element-by-element, into a linear array of N parallel-traveling white light beamlets 102. The term "white light beamlet" includes not only a white light appearance in its literal sense, but also includes colors produced by varying combinations of red, green and blue light. The number of beamlets N is between 2 and any integer power of 2 (inclusive).

As detailed further herein, the linear array of N parallel traveling output beamlets 102 is reflected and refracted by the optical scanning/projection system 300 to simultaneously scan a swath of N lines across the projection screen 400, and to raster M successive swath scans down the projection screen

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400. Thus, in contrast to the line by line-by-line raster scan of a conventional single beam CRT as shown in Figure 2, the color image projection system 10 simultaneously scans a swath of N lines across the projection screen 400 as illustrated in Figure 3.

5 The data processing system 200 reformats an input video or graphic signal 201 (hereinafter collectively referred to as "video") into swaths, the total of which create a screen full of pixels. Each
10 of the independently addressable sets of beamlets 110, 112 and 114 of the three primary color linear array sources 104, 106 and 108, respectively, is modulated accordingly so that the combined N white light beamlets of the white light linear array source
15 recreates the input picture by filling the projection screen 400 with illuminated pixels, swath-by-swath, frame-by-frame.

The data processing system 200 includes a video input converter 202 which converts common types of
20 picture signals, such as composite video, analog RGB or computer graphics into digital Red, Green, Blue (RGB) data. Chip sets for performing such conversions are commercially available from the Signetics Division of Philips Semiconductors of
25 Sunnyvale, California as further detailed herein with reference to Figure 8A.

In the preferred embodiment of the invention, a microprocessor-based controller 204 in conjunction with a memory 206 is employed to control the video
30 input converter 202 via a bus 208. In this way, the decoding and formatting of the video signal are under microprocessor control. The bus 208 allows decoded timing information such as sample clock (horizontal sync) and frame reference to be passed to other
35 portions of the color image projection system 10. In

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addition, the bus 208 may be utilized to transfer control information to between an external computer or controller and the image projection system 10. For example, an external host computer connected to the bus 208 can be used to control the amount of hue, saturation, tint, and other composite NTSC attributes.

The video input converter 202 provides a red digital data stream 210, a green digital data stream 212 and a blue digital data stream 214. Each of the streams 210, 212 and 214 is a serial pixel stream 8-bit wide, that provides a grey scale of $2^8 = 256$ levels for each color data stream. Each stream has a raster format that fills a frame line-by-line. Since the color image projection system 10 fills a frame with one or more swaths, the single serial pixel stream needs to be reformatted into multiple (e.g. N) serial pixel streams, one for each of the N output beamlets of the white light linear array source 100. Stated differently, the digital data streams 210, 212 and 214 need to be converted from a single beam raster scan format as shown in Figure 2A to an N-beamlet raster scan format as shown in Figure 3B. Since N can be quite large, for example 64, 128 or even higher, it is preferable to multiplex the 8-bit parallel path for the grey scale pixel information for each of the N lines in the pixel stream. In the preferred embodiment of the invention, 8-bit grey scale pixel information is converted to multiplex form by a set digital processors 216, 218 and 220. For example, the serial sequential pixel stream of digital Red data 210 (8-bits wide represents all the first pixels in order from 1 to N for a swath, followed by all the second pixels of the swath, etc.) is processed by the digital processor 216 and emerges

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as a single multiplexed pixel stream. Similarly, the serial sequential pixel streams of digital green data 212 and blue data 214 are processed by a digital processor 218 and a digital processor 220, respectively.

The digital processors 216, 218 and 220 also serve to convert data from a selected video standard (e.g., resolution, aspect ratio) to that of the color image projection system 10 as detailed further herein, particularly with reference to Figures 8A through 10D. The image projection system 10, however, may be used without such data conversion when displaying an image format which matches or corresponds to the preselected image format of the image projection system 10.

A multiplexed red pixel stream 222 from the digital processor 216 is subsequently used to modulate the corresponding individually-addressable output beamlets of the red linear array source 104. Similarly, the green and blue multiplexed pixel streams 224 and 226, respectively, are used to modulate the corresponding output beamlets of the green and blue linear array sources, 106 and 108, respectively. In the preferred embodiment of the invention, such modulation is performed using a pulse-width modulator (PWM) 228, for the red component. The pulse-width modulator 228 and its advantages are described in further detail with reference to Figure 10. Similarly, a pulse width modulator 230 operates on the green component, while a pulse width modulator 232 operates on the blue component.

After the white light linear array source 100 is modulated by a set of N-pixel streams 234, 236 and 238, the emerging linear array of N parallel-

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traveling modulated white light beamlets 102 is optically processed to produce, by way of the optical scanning/projection system 300, a raster scan, having one or more swaths, on the screen 400.

5 In one preferred embodiment of the invention, the optical scanning/projection system 300 comprises a mirror 302 which is integral to a galvanometer 304, and rotating regular polygon mirror 306 which is
10 driven by a motor 308. The galvanometer 304 and the motor 308 are each controlled by a raster generator servo 310. The rotating regular polygon mirror 306 has as at least many facets as the number of swaths M required to generate the full picture frame. In
15 further detail, starting from a first facet, a parallel set of N beamlets 102 strike the mirror 302. The beamlets 102 are reflected by the mirror 302, reflected again by the polygon mirror 306, then refracted by a set of projection optics 312 to
20 thereby scan a first swath across the screen 400, starting at the left top of the picture frame. This embodiment uses a 9 facet regular polygon mirror having a diameter of approximately 3 inches. This results in a polygon scan efficiency of 0.675. The vertical scan efficiency is set by the performance
25 capability of the galvanometer and the number of inactive facets during a field cycle. Inactive facets are used to allow the galvanometer to retrace before starting a new field. A reasonable scan efficiency for a galvanometer is in the 70% (less
30 than 80%) range. Consequently, at least 3 inactive facets are required during a scan cycle with a corresponding galvanometer scan efficiency of 0.727 ($8/8+3$). The resulting overall scan efficiency is 0.49 (the product of 0.675 and 0.727) and the
35 throughput efficiency of this system with a

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transmission of 0.60 would be less than 0.294 (the product of 0.60 and 0.49). The 9 facet/3 inch polygon system with a 72.7% vertical scan efficiency requires a spin rate of 4400 RPM to produce an 8
5 swath field at a 60 Hz field rate.

The net result is that a 128 element laser with a total power of 3.4 Watts per laser diode array (26.6 mW per element) will provide 3 Watts of power at the screen (the product of 3, 3.4 and 0.294).
10

An important feature of the invention is the use of angled facets to provide vertical rastering. Conventional projection systems typically employ a rotating regular polygon (the surface normals of all facets being perpendicular to the rotational axis)
15 for horizontal scanning, and a second rotating mirror (usually a galvanometer) to raster the beam vertically down the screen. This adds complexity and cost to the system.

In another preferred embodiment of the optical scanning/projection system 300 as shown in Figure 1, the galvanometer 304 with its integral mirror 302 is employed in tandem with the rotating irregular
20 polygon mirror 306.

As detailed in Figures 3A and 3B, the surface normal to the second facet is tilted relative to the surface normal to the first facet to thereby index the second swath scan vertically down the screen, so that the bottom horizontal line of the first swath scan is contiguous with the top horizontal line of
25 the second swath scan. The surface of each successive facet is similarly tilted with respect to the preceding facet to raster successive swath scans vertically down the screen until M swath scans are made and the whole picture frame is filled, whereupon
30 the first facet once again begins to produce the next
35

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frame. One or more facets (in excess of the number M) may be utilized in the polygon mirror 306 to accommodate any vertical blanking interval signal included within the video signal 201.

5 The rotating irregular polygon mirror 306 by itself, however, will only produce M contiguous swath scans for each picture frame. The galvanometer 304 and its integral mirror 302 provide interlaced swaths to thereby display interlaced video signals. The
10 galvanometer 304 produces a minor shift in the angle of the mirror 302 to thereby offset successive fields. For example, in a 2:1 interlace, the shift amounts to half a horizontal line spacing. In 2:1 interlaced systems, a picture frame is formed by the
15 eye integrating "fields", where the field rate is double that of the frame rate.

 In this preferred embodiment of the invention as shown in Figure 1, an 18° scan is used to minimize the swath mismatch error generated by the pyramidal
20 tilt of the irregular polygon mirror facets. The nominal swath mismatch error for an 8 swath 18° scan with a 1.8:1 aspect ratio is approximately $\pm 1/5$ of a line pitch. Limiting the effect of fabrication errors in the pyramidal tilt to less than $\pm 1/5$ of a
25 line pitch requires tolerances tighter than ± 3 arc seconds. The dynamic errors of polygon wobble and facet to facet timing also require accuracies of approximately ± 1 arc second to be negligible contributors to raster impairment. Swath mismatch
30 and pyramidal error contribute a peak to peak error of $\pm 2/5$ of a line pitch.

 An 8 swath field requires an irregular polygon mirror with a multiple of 8 facets. The scan efficiency for an 8 facet polygon mirror with a

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horizontal scan of 18° and a diameter of approximately 2 inches is 0.2. To double the scan efficiency to 0.4 with a 16 facet polygon requires a diameter of approximately 6 inches. Such a diameter increase, however, increases the mass of the mirror by about fifty percent.

An important feature of the image projection system 10 of the present invention consists of the simultaneous scanning of N pixel lines of the picture frame. This method greatly reduces the angular velocity of the polygon mirror 306 required to fully illuminate a picture frame at a specified frame rate. Each polygon mirror facet is employed to scan swath-by-swath instead of line-by-line. This reduces the product of the number of facets and the angular velocity by a factor equal to the number of lines N in the swath in contrast to an N=1 system. For example, if a swath of 128 lines is scanned at a time, a 10-facet polygon mirror 306 needs to be employed to produce a picture frame of 1280 horizontal lines. If desired, however, the polygon mirror 302 together with an N=1 white light source can be utilized to produce a frame having a number of lines equal to the number of successively angled reflective facets of the mirror 306 without the need for a separate vertical deflection mirror.

Video standards with different vertical scan rates or frame rates are easily accommodated by varying the angular velocity of the polygon mirror 306. This method provides a tremendous advantage over magnetically deflected systems where scan rate changes require differently configured yoke windings and compensation for the resultant changes in yoke inductance. The raster generator servo 310 obtains timing signals from the bus 208 and ensures that the

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mirror 306 turns and that the mirror 302 is deflected at desired rates, both in synchronization with the raster scan.

Referring now to Figure 4, a first alternative embodiment 500 of the optical scanning/projection system 300 of Figure 1 is shown. In particular, where the number of lines to be projected for a desired frame is equal to N , the galvanometer 304 with its integral mirror 302, simply reflects the N lines of white light to thereby scan the N lines across the screen 400. Unless required for interlacing, no vertical rastering is necessary in this embodiment. This arrangement provides a significant decrease in noise, vibration and heat which result from the motor 308 and the rotating polygon mirror 306. In addition, mismatch errors due solely to the polygon mirror 306 are eliminated.

Referring now to Figure 5, a second alternative embodiment 600 of the optical scanning/projection system 300 of Figure 1 is shown. In the second alternative embodiment 600, the polygon mirror 602 consists of a mirror where the surfaces of each of its facets are parallel to the axis of rotation of the mirror 602, in contrast to the mirror 502 utilized in Figure 4.

In the embodiment shown in Figure 5, where the desired number of lines to be projected as a frame is equal to N , and no interlacing is required, no vertical rastering apparatus is necessary. However, if the desired number of scan lines in a frame is less than N , then the mirror 602, in combination with a galvanometer (not shown) for vertical detection would provide the desired number of scan lines in a frame.

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Thus, this third alternative embodiment would appear as that of the optical scanning/projection system 300 at Figure 1, except that the galvanometer 304 and mirror 302 combination would be utilized to provide vertical deflection instead of merely interlacing, and the polygon mirror 306 would provide only horizontal deflection since the facets of the polygon mirror are not tilted with respect to each other. With this third alternative embodiment of the optical scanning projection system the angular velocity of the mirror 602 would be reduced by a factor of N over systems where $N=1$.

White light Laser Linear Array Source

Figures 6A, 6B and 6C illustrate in detail the red laser array 104 of Figure 1. Except for the frequency of the light emitted from the array 104, the array 104 may be utilized as the green laser array 106 and the blue laser array 103. Mounted in alignment on a base substrate 700 are: 1) a monolithic linear array of individually-addressable stripe semiconductor laser diodes 702; 2) a linear array of micro-optical lenses 704; 3) a linear array of integrated electro-optic modulators 706; 4) a linear array of micro-optical lenses 708; 5) a linear array of nonlinear harmonic converters 710 and 6) a linear array of output beamlet collimation micro-optic lens 712. The monolithic linear array 702 includes a set of N (e.g. 64, 128,) individually-addressable, semiconductor stripe laser diodes 714. The base substrate 700 provides mechanical support and registration for the laser diodes 702, the lenses 704, the modulators 706, the lenses 708, the harmonic converters 710 and the micro-optic lens 712.

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In a first preferred embodiment of the red laser array 104, the monolithic array of semiconductor stripe laser diodes 714 are grown from the AlGaInP quaternary III-V semiconductor material system to directly emit the radiation of the desired red color. In this embodiment, the modulation of the output laser beam of each stripe diode is imposed by directly modulating the drive current at each diode. The modulated radiation from each of the semiconductor stripe laser diodes of linear array 702 is then immediately collimated by the micro optic lens 712 to form the linear array of red output beamlets of the red primary color array source. In this preferred embodiment the micro optical lenses 704, modulator 706, the electro-optic micro-optic lenses 708 and the harmonic converters 710 are omitted.

In a second preferred embodiment of the red laser array 104, radiation from the linear array of AlGaInP red stripe diodes 714 is modulated by it passing through the linear array of electro-optic modulators 706, and immediately collimating the modulated linear array of output beamlets using linear lens array 712; As in the first preferred embodiment the micro-optic lenses 708 and the harmonic converters 710 are omitted.

In a third preferred embodiment of the red laser array 104, the monolithic array of semiconductor stripe laser diodes 702 is grown from the InGaAsP quaternary (or InGaAs; InGaAs ternary) III-V semiconductor material systems to emit light at twice the wavelengths of the desired red (green; blue) primary color, for example, at an infrared wavelength of 1300 nm for red (1040 nm for green; 960 nm for blue). In this embodiment, the modulation of the

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output of each stripe diode is imposed by directly modulating the diode drive current at each of the diodes 714. The modulated radiation from each of the semiconductor stripe laser diodes 714 is then immediately coupled to the linear array of stripe nonlinear harmonic converters 710 via the linear array of micro-optic coupling lenses 708. The linear arrays of stripe nonlinear harmonic converters for red, green, and blue primary color array sources comprise quasi-phased-matched waveguides grown, for example, from the LiNbO_3 , LiTaO_3 , KTP, KNbO_3 material systems. The linear array of red (green; blue) modulated output beamlets from the nonlinear converter array is then collimated by linear lens array 712.

In a fourth preferred embodiment, a variant of the third preferred embodiment, the modulation of the infrared radiation from the array of semiconductor stripe laser diodes 714 is achieved by passing it through the linear array of electro-optic modulators 706 prior to passing it through the integrated linear array of stripe nonlinear harmonic converters 710 and the linear array of output collimating lenses 712. The linear array of electro-optic modulators 706 consists of N Mach-Zehnder interferometers grown, for example, from the LiTaO_3 , LiNbO_3 , KTP material systems. In further detail, Figure 6B illustrates a set of integrated electro-optic modulators 714, 716 and 718 followed by a nonlinear harmonic converter 720, 722 and 724 respectively. Although not illustrated in Figure 6A, there is one electro-optic harmonic converter for each of laser diodes 702.

Referring now to Figure 6C, when each of the laser diodes 702 which is modulated directly by varying the current to such diode(s), the electro-

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optic modulator 706 is not required. Instead, the harmonic converters 710 are utilized as illustrated in detail in Figure 6C. In particular, these converters 710 consist of a non-integrated array.

5 Referring now to Figures 7A, 7B and 7C, one preferred embodiment of the white light linear array source 100 of Figure 1 is illustrated in greater detail. The red, green and blue laser linear arrays 104, 106, and 108 respectively are shown along with
10 the optical combining mirrors 804, 806 and 808 are mounted onto a base plate 800 that is cast from a lightweight material such as aluminum or magnesium. The base plate 800 functions as a heat sink for the laser arrays 104, 106 and 108. The base plate 800
15 further provides datums for registration of the arrays 104, 106 and 108 to the base plate 800. An optical combiner 802 includes the mirror 804 and the pair of dichroic mirrors 806 and 808. The optical combiner 802 combines corresponding 1-to-N collimated
20 beamlets 110, 112 and 114 from each of the linear arrays 104, 106 and 108 into the single linear array of N parallel-traveling, collimated white light beamlets 102. Each of the three primary color linear array sources 104, 106 and 108 is fabricated as an
25 integrated linear array subassembly as shown in Figures 7A, 7B, 7C and 7C.

Referring now to Figure 7B there is shown a front view of the white light laser array source 100 of Figure 1. Figure 7C illustrates the combination
30 of a set of electro-optic modulators (of Mach-Zehnder interferometer type) 810, 812 and 814 and their associated integrated harmonic converters 816, 818 and 820 respectively.

Figure 7C corresponds to Figure 6B.

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Referring now to Figure 7D, an end view of the harmonic converters 710 of Figure 6A is shown.

Figure 7D corresponds to Figure 6C.

5 A second preferred embodiment of the white light linear array is described further herein with reference to Figure 8C.

Data Processing System

10 Figures 8A, 8B, 8C and 8D illustrate overall data flow and basic raster generation of the data processing system 200 of Figure 1, within the image projection system 10. The video signal 201 enters the video input converter 202, and whether the signal is standard NTSC, RGB component, or VGA computer, in all cases the signal is converted to 8-bit digital
15 RGB video, as three separate data streams. The video input converter 202 includes an analog-to-digital converter 900, a phase lock loop 902 and a composite component decoder 904. Such video converters are well known in the art and may be constructed from
20 commercially available chip sets. In particular a Philips Semiconductor TDA8708 analog-to-digital converter together with a Philips Semiconductor SAA 7191 luma, chroma processor/sync and clock processor and a Philips SAA 7197 clock generator circuit
25 operate to separate a analog composite video signal into digital luminance, chrominance, horizontal and vertical signals. A TRW TMC2272 digital colorspace converter/corrector converts these digital signals and provides separate red, green and blue digital
30 outputs. Referring now to both Figures 8A and 8B, this red, green and blue then passes into a memory 906 which is used for horizontal time base correction and as an input memory (only several lines needed) necessary for the resizing processor to operate in a

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zoom mode. The stabilized red, green and blue digital data is clocked out on demand from a set of memories 908, 910 and 912 into an image resizing processor 914 for data resolution translation to match the fixed resolution of the white light laser array source 100 and the optical scanning/projection system 300. The image resizing processor 914 contains a set of separate processors 916, 918 and 920 for the red, green and blue components respectively.

After being resized and remapped, the red, green and blue video components are input into a memory store 922 where re-rasterization into 128 line swaths takes place. The memory store 922 contains a set of separate swath memory banks 924, 926 and 928 for the red, green and blue video components respectively. Referring now to both Figures 8B and 8C, after re-rasterization groups of scan line data (swaths) are read out from the memory store 922 in a parallel multiplexed format and provided to a pulse modulator IC bank 930 to form a pulse width modulated signal which represents the intensity of the video. Each binary pulse-signal which represents the illumination of the RGB components along a scan line is applied to the lasers. The pulse modulator IC bank 930 includes ROM memory which stores data that converts each eight bit stream into a corresponding laser intensity. The pulse modulator IC bank 930 includes a set of four pulse width modulators 932, 934, 936 and 938 for the red video components, a set of four pulse width modulators 940, 942, 944 and 948 for the green video component and a set of four pulse width modulators 950, 952, 954 and 956 for the blue video component. Since for a given power input, each laser diode can have a different output, the ROM can be modified to

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so that each laser diode operates within a predetermined intensity range. A polygon mirror 30 and a flat mirror 958 provide horizontal and vertical deflection respectively of each swath.

5 Sync Timing Analysis

 There are other standard signals that the various input modules generate and pass to the image projection system 10. They are the sampling clock, which is derived from the horizontal sync, and a
10 signal called FREF, Frame Reference. This is a signal that is usually an active-low-going edge on the first pixel of active video, of the first line, of the first field. All the other synchronizing signals can be derived from this one edge, including
15 horizontal sync, vertical sync, blanking, and chrominance field reference.

 A system control bus 960 is a bi-directional computer bus interface that allows control and setup preference data to be transferred to go between a
20 system control microprocessor 962 and the various subsystems of the image projection system 10. In particular the system control microprocessor 962 responds to an interrupt from an input device (such as external computer), reads the corresponding input
25 value, stores it a RAM 964, checks a ROM 966 for information regarding all the correct destinations necessary for executing the values change correctly, and then performs a write function to all the destinations in sequence.

30 A system timing generator 968 generates all synchronization signals needed by the image projection system 10 for data exchange, video data synchronization between all sub-systems, phase lock, encoding and decoding of multiplexed signals, and

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direct modulation sub-pixel high speed clock references.

System Control Bus

5 In further detail, the system control bus
960 is a bi-directional computer bus interface.
Using an NTSC video signal as an example, the image
projection system 10 must be able to control the
amount of hue, saturation, brightness, and contrast
in the same way a person usually adjusts a home CRT
10 video monitor for preferences to have the picture to
"look" right. In one embodiment of the present
invention, values corresponding to specific amounts
of hue, saturation, brightness and contrast could all
be pre-set values that are stored in a ROM/RAM 961
15 allowing an operator to set-up either externally or
from a front panel of the image projection system 10.
Information provided from the user passes through the
system over the control bus 960 to the appropriate
subsystem.

20 In the preferred embodiment of the invention all
of the subsystems of the image projection system 10
have the same feature bus interface, so any plug in
can send and receive the same type of data. Each
subsystem has a unique destination address, so a user
25 could select any subsystem as the source from a
remote control or from the projector front panel.
The system control bus 960 is an operating
information bus that connects between all the
different parts of the image projection system 10 as
30 well as to an external host computer. In the
preferred embodiment of the invention, the host
computer is an IBM AT type with a serial I/O (RS-
232) port.

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Video Input ConverterNTSC RS-170A

With reference now to the video input converter 202, NTSC RS-170A, is broadcast to U.S. households or output from VHS tape recorders or videodisc players. As specified in Figure 10C, an important specification for NTSC video is that the aspect ratio is 4:3, which is not a wide screen aspect ratio. In the preferred embodiment of the invention, an NTSC interlaced video signal is digitized to a resolution of 768 pixels horizontally across a line, for high quality representation, and between 480 and 484 active vertical lines. The chrominance signal is encoded separately from the luminance signal and is lower in resolution. Because of known artifacts in the digitized video signal, digital image enhancement which is well known in the art, may be utilized to improve picture quality for a large projected image. Alternatively, digital signals may be provided directly to the composite/component decoder 904. In such an instance, all signals paths with the image projection system 10 are digital.

With reference now to the video input converter 202, a typical video decoder for a broadcast NTSC signal has an input which accepts a composite signal that contains color and luminance information encoded together with a sync signal. Prior to decoding, the sync signal is first stripped off of the composite analog signal. The horizontal sync signal is used to synthesize a sampling clock, as the reference pulse for the phase lock loop 902 as it occurs once every horizontal line. Next the composite video (luma and chroma) is digitized by the analog-to-digital converter 900 and the chroma-luminance separation is performed done digitally by the composite/component

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decoder 904. Decoding digitally is much cleaner than analog decoders that are now used in consumer TV receivers. With the analog method, filters are used to separate the chrominance components from the composite signal. Such analog filters introduce distortion and reduces overall signal bandwidth. Luminance and chrominance samples decoded from the composite digital signal are applied to a digital matrix (such as the TRW TMC2272 Digital Colorspace Converter Corrector) to obtain a RGB color space signal. The video passes through the image projection system 10 in RGB form.

In the preferred embodiment of the invention, an NTSC analog composite video signal is provided to the video input converter 202, and image data is supplied by the output of the video input converter 202 to the subsystems of the image projection system 10 in RGB color space. RGB is an equal luminance-chrominance-resolution format, meaning that luminance changes and color changes can both occur at the same rate in terms of bandwidth. An NTSC analog composite signal has luminance (Y) and color difference signals (U and V), however, NTSC is not an equal resolution format. The luminance (Y) typically has 4 MHz resolution, the color difference signals (U, V) usually have 1.5 and about .5 MHz resolution respectively. Thus, NTSC saves bandwidth by carrying less color information. Video in the form of graphics and high resolution text, however, is all equal resolution. Within the video input connector 202 the luma- and color-difference signals are converted to RGB, in order to more easily display equal resolution video sources.

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1050/29.9 and HDTV 1125/30

With reference now to Figures 8A-8D, 10A and 10B, a second preferred embodiment of the video input converter 202 is described. The second preferred
5 embodiment of the video input converter 202 is configured to process HDTV video signals, such as the 1050 2:1 NBC proposed standard detailed in Figure 10B and the 1125 SMPTE 240M standard detailed in Figure 10A. Video signals conforming to either of these
10 standards are already RGB form, usually on three separate cables. Thus, the analog-to-digital converter 900 includes three separate analog-to-digital converters. By utilizing separate analog-to-digital converters, cross-contamination is
15 minimized. This results in three paths, 8 bits each of R, G, and B. The sync and clock signals are generated in the same way for all standards in the same way as for NTSC signals.

Usually the HDTV standards signals carry H and V
20 separately, sometimes composite, and often on a separate wire. The HDTV standard of Figure 10A utilizes a "tri-level sync." Although difficult to detect, and vulnerable to noise, it is a bi-level type of standard which under normal conditions is
25 easily decoded as is well known in the art.

In contrast to the first preferred embodiment of the video input converter 202, the second preferred embodiment (for HDTV) is actually simpler since the components are in a separate form and the analog-to-
30 digital converters simply need to run at a higher clock rate. The phase lock loop 902 insures that the sampling clock runs at a higher clock rate.

A third preferred embodiment of the video input converter 202 is configured to process VGA computer
35 graphics signals. VGA graphics information is

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typically stored in the RAM on a display card within a computer in look-up table mode. This means that between 8 and 16 bits, up to 24 bits on a digital signal bus (within the computer) is used as an address of a location in a RAM. The address in the RAM is programmed with any one of millions of color possibilities, but only some limited number available at one time. This type of scheme in graphics is used to allow any set of color palettes.

Given the limited number of color possibilities available within a particular color palette, the colors loaded into memory must be carefully selected to avoid an unnatural looking display. Using analog VGA as input to the video converter 202 would require redigitizing a previously digital signal (data) resulting in degradation from the original digital signal. Therefore, in this third preferred embodiment of the video input converter 202, the bit stream from a VGA feature bus connector of the computer is used as a clean source. Using this signal as the address of a look-up table, which would be the inverse of the color table data normally found on the VGA card, provides RGB directly in digital, and is compatible with the RGB format utilized by the image projection system 10.

In further detail the look-up table is loaded with the same values as in the VGA card within the computer. The VGA feature bus connector and the computer's serial interface connector is used with a software interface routine to download the color table data from the computer into the image projection system 10. In the preferred embodiment of the invention, a standard cable with a pair of D9 type connectors for serial output links to an AT type computer with a VGA interface card in the image

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projection system 10. This yields identical colors on both a display connected to a computer and on the screen 400 of the image projection system 10. Clock, horizontal and vertical drive (sync) signals originate directly in digital form on the VGA feature connector. These signals are utilized to generate the input clock signal and FREF.

Time Base Corrector Memory

The preferred embodiment of the invention utilizes time base correction in order to move accurately display video signals originating from video tape or other video sources. The time base of such signals is altered by their particular reproduction system because their reproduced video signals have jitter generated as the tape moves across the head and because of minor variations in tape speed. Furthermore, because tape heads are not perfectly round, there is uneven stretch in the tape thereby causing minor shifts in each horizontal line. And even during a single line there are variations in tape speed sufficiently great to adversely affect the chroma and luma characteristics of the video signal. In home TV receivers, the deflection is designed to overscan the video raster larger than the size of the tube. As a consequence, the user never actually sees the ragged edge of the video. Some part of it is actually hidden off the screen by the plastic bezel plate. If there is a minor shift in horizontal lines, the ragged edge is hidden. This shift degrades the overall image, vertical resolution and causes improper color signal decoding. However, most television viewers remain oblivious to such fidelity problems.

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With a projector displaying a large image, however, such minor shifts in horizontal lines are more noticeable. For example, in order to show an entire image on a huge rectangular screen, the raster cannot be overscanned. Instead, the image is horizontally re-aligned. Modern TV's use what is known as a "fast horizontal AFC" typically .5 millisecond (as compared with 5-7 milliseconds found in older sets). Using a small time constant in a horizontal oscillator phase lock loop allows the scanning system to 'instantaneously' correct for minor changes in horizontal timing so the ragged edges are partially corrected. However, such a technique is not useful with an electromechanical scanning system (as opposed to electromagnetic) since an electromechanical scanning system has a significant mass which is difficult to vary with such corrections.

A similar process of disturbing the time base is often used as a copy guard protection method against unauthorized home VCR recording. The headwheel in the VCR is much like the scanning polygon mirror. Video recordings which contain copy guard protection are often pre-distorted with a small amount of horizontal and vertical jitter so a TV receiver sync scanning circuit can fast track the error, but a VCR's headwheel cannot. Thus, a video time base corrector is utilized for the preferred embodiment of the present invention.

With reference now to Figure 813, the time base corrector and buffer memory 906 is described in further detail. The time base corrector and buffer memory 906 samples a few scan lines and reads them into memory with control information. Digital logic contained in the time base corrector and buffer

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memory 906 determines that the horizontal sync has deviated from a standard synthesized internal horizontal sync, and in response the time base corrector section re-shifts the lines by some minute amount so that they will all line up again. The lines are then read out of the buffer memory section in perfect synchronism for display as a perfect raster.

Image Resizing

The image projection system 10 of the present invention displays a variety of image formats. This places a tremendous burden on the scanning electronics because there are different numbers of pixels per lines, different numbers of lines, different blanking times between retrace. To display all raster formats with the restrictions imposed by a predetermined of laser light sources is impractical. Turning off different combinations of laser light sources to obtain a fully lit raster causes uneven line spacing. There will always be some multiples of frequencies and/or scan lines that an optical scanning system cannot accommodate.

In order to overcome this problem, the image projection system of the present invention uses the image resizing processor 916 to convert different numbers of pixels per line and different numbers of scan lines and convert them into one common resolution. As graphically illustrated in Figure 9, with this method, it becomes possible to optimize the image projection system 10 for a predetermined image size. In addition, the conversion method utilized in the present invention corrects for images existing in different aspect ratios. If an optical scanning projection system is configured to produce a specific

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aspect ratio, for video usually between 16:9 and 4:3, it is necessary for some additional horizontal shrinking and zooming, depending on which operation is necessary to correct aspect ratio raster.

5 In further detail, the image resizing processor 914 performs two image resolution transformations, one for resolution matching and one for aspect correction.

10 The image resizing processor 914 receives image input at any number of pixels per line and any number of lines and performs an interpolation function in the digital domain to thereby yield more or fewer pixels per line and more or less lines per frame. The image input is converted to a common format for
15 the scanning/display generation and with a fixed number of lasers in the array and a mirror with a fixed number of facets. In the preferred embodiment of the invention, the image resizing processor 914 converts all inputs to a common format of 1280 pixels
20 by 1280 lines which is based upon 128 laser diodes in each laser array.

 In the preferred embodiment of the invention, the image resizing processor 914 performs fractional re-scaling, horizontal and/or vertical, independently
25 done in either dimension, horizontally and vertically. Any image signal can be matched to the fixed 1280 x 1280 format. In operation, complex interpolation and variable bandwidth filter algorithms perform resizing of image data in real
30 time at high speed.

 The image resizing processor 914 accepts control from the system control bus 960, the same bus described with respect to the video input converter 202 and image projection system 10. The image
35 resizing processor 914 loads line and pixel

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parameters, based upon required resizing factors obtained from an internal table which contains values corresponding to each of the various standards. This results in the correct aspect ratio as well as the correct number of lines and pixels per line. Image resizing techniques which are suitable for use in connection with the preferred embodiment of the invention are disclosed in the paper entitled "Video Resizing - how to make bigger/smaller image the best it can be!" by James H. Arbeiter, Proceedings, Electronic Imaging International, September 29 - October 2, 1992, Hynes Convention Center, Boston, Massachusetts, which paper is incorporated herein by reference.

There are a variety of vertical frequencies for video signals. There is 59.9 for NTSC and the proposed 1050 HD standard; there is 1125/60 - which is actually 1125/30 because it is a 30 Hz frame rate - and there is VGA computer graphics, which can vary vertically between 40 and 70 Hz.

For the image projection system 10 to accommodate various vertical scan rates, the polygon motor is rotated at different speeds, and the flat mirror is deflected so that it provides vertical deflection at the same rate as that of the input video signal. Whatever image signal is displayed, the scanning optics remain the same, because it is always the same amount of data, input re-sized to fixed scanning resolution, the difference is in data rate only. A different vertical scanning rate relates to scanning out the whole field or frame faster or slower. It can be thought of as the same raster going on the screen, it is just scanned out a little faster or slower, based on the vertical rate, which is the field or frame update rate.

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The image resizing process coupled with the variable speeds of the polygon drive motor and the vertical deflection galvanometer and its associated control electronics 310 provide a method to handle any image format that is input to the image projection system 10. All the necessary parameters can be pre-coded into ROM tables in order to unburden the system from having to perform the computations necessary to obtain them.

10 Swath Memory Banks

In the preferred embodiment of the invention, sufficient brightness is obtained by scanning out many lines in parallel.

15 Since all current video standards are in CRT raster format, meaning a single data stream for a single beam which scans back and forth, the present invention includes a method of converting from a single beam input format to a raster scanning of multiple laser beams scanning out simultaneously.

20 In accordance with the present invention, all of the lines needed for the parallel scan are stored in the memories 924, 926 and 928 and read out to produce parallel scanned lines. The first pixel of all the lines is read from the memory first and then the
25 second pixel for all lines and so on at $1/N$ speed, where N =number of parallel lines. All the laser diodes are active at the same time. One preferred method for performing the function is to use two memories. A first memory stores a group of N lines
30 while a second memory outputs a group of N lines in parallel. Then they "ping-pong" back and forth. Video is read into either memory, the addresses sequentially incremented, and at the output the video is read out in a line-parallel fashion.

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It is almost impractical to use 128 8-bit paths going out of the memory. A method for alleviating this problem would be to read out from the memory in a different order, meaning read out the N pixels for the N lines, then N pixels of N+1 lines. This is called a multiplexed format where data appears sequentially on a single wire and the order of the sequence is known and decoded at the destination. Since the data goes out N times as slowly, N pixels can be read out in the same amount of time and then each one shown them as the first pixel on all parallel sources. The data rate going into either memory is the same as the data rate going out. If we had different data rates, the memory would either overflow or become exhausted.

Pulse Width Modulators

In the preferred embodiment of the invention, the image projection system 10 uses 128 lines scanned out in parallel. With three (R, G, B) diodes per pixel, this results in a total of 384 active video channels. These channels originate as 384 equivalent (multiplexed) data streams from scan-converting frame stores 924, 926 and 928. In the preferred embodiment of the invention, each channel uses 8-bit equivalent resolution. Each such digital signal is converted to an analog signal in order to drive the laser diodes to reproduce the time varying pixel amplitude (and color) for each of the 128 scan lines. 384 digital to analog converters in any system is an extremely large number of devices, requiring large PC board real estate, tedious calibration, and linearized driver electronics of sufficient power and bandwidth.

The preferred embodiment of the present invention uses pulse-width modulation to provide a

-44-

full power current source for each laser with a time varying pulse train (duty cycle), so that the average power delivered to any diode over the pixel time will correspond to light the diode at specific amplitude intensity. As illustrated in Figure 11, this would be the same exact amplitude that the D/A converter would produce for a given 8-bit value, which in fact is the R, G, B intensities of video for a given pixel. This is advantageous for several reasons.

Switch mode electronics eliminates the need for D/A converters, highly filtered power supplies, linearized driver electronics, and costly setup and calibration. Switch mode logic uses less power than its analog counterpart, since there is little power dissipated in output transistors themselves as they are either fully on or fully off. Although specific examples of pulses for generating specified intensity levels are illustrated in Figure 11, different numbers, timing and amplitude of each of the pulses may be utilized to deliver a predetermined amount of energy to each laser diode over a predetermined period to thereby establish the (perceived) intensity of the output of such laser diode. Alternatively, such pulses can drive Mach-Zehnder interferometer modulators to control the intensity of each beam.

Switch mode pulse code modulation (PCM) is normally not applied to digital video systems. The reason is that for 8-bit resolution (256 levels of intensity) and 4 MHz video bandwidth, the switching rate would have to be extremely fast. For example, if a pixel needed to be minimum intensity, or $1/256$ as bright as full intensity (256 levels), and if response were linear, a reasonable assumption might be that a pulse that was full on for $1/256$ of the overall pixel time would be needed. That would

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require a clock 256 times as fast as the pixel clock used for digitizing the input video data stream.

Most drivers could not switch sufficiently fast for this application and would become slow-rate limited.

5 In addition, the digital logic to generate such fast pulse trains does not exist in any off-the-shelf or cost effective form.

However, the image projection system 10 of the present invention scans out swathes of

10 line-slowed-down video. In the case of 128 swathes, the line time is 128 times longer. The switch rate drops almost by a factor of 128, which is within the normal operating range for CMOS type IC logic. This range is very reasonable for operation of a
15 switch-mode driver. For pulse width modulation, the amount of light energy resulting from the duration of some number of pulses is proportional to the amplitudes of each video sample (pixel).

In order to achieve performance acceptable to
20 most viewers it is necessary to have more than enough on-off cycles during one pixel time to produce the minimum of intensity values for good quality image reproduction. Because the image projection system 10 has such a large display and excellent beam/optics,
25 and the human eye is extremely sensitive to grey level resolution, and in the preferred embodiment of the invention, 256 grey levels are utilized instead of the 128 levels illustrated in Figure 11. The modulation used will at any sampling instant to
30 produce a specific pulse duration, based on an 8-bit intensity code, to generate the amplitude value of the baseband signal. For any analog signal the pulse width modulation (PWM) varies linearly between the two extremes of non-intensity to full-intensity. The
35 widest cumulative pulse time represents the maximum

-46-

energy delivered and the narrowest cumulative pulse time represents the minimum energy, or minimum intensity.

5 The duration of the pulse varies as a function of the amplitude of the image signal. The eye has the ability to integrate modulated light pulses into an averaged perceived value. However, it may be desirable to use an integrator to smooth the sharp step inherent in switched outputs to produce a more
10 average value. Alternatively, using a finer pulse step increment may eliminate the need for a post filter. This scheme requires less components than analog integrators. Also, pulse modulation schemes can scale over wide frequency ranges, where the
15 analog components are generally specified for only one sample frequency.

 Using this pulse modulation method simplifies the amount of analog electronics and wired interconnects in the image projection system 10. In
20 addition, many PCM units could be placed on a single IC device (typically 32 modules).

 In the embodiment shown in Figure 8C, output from the red laser array 104, the green laser array 106 and the blue laser array 108 is combined using
25 different combining optics than in the embodiment shown in Figure 7A-7D. In particular, parallel beams of primary color are reflected by a mirror 951, parallel beams of a second primary color are reflected by a mirror 953, and parallel beams of a
30 third primary color are directed to a combining cube 955 which operates to combine corresponding parallel beams of each primary color.

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Raster Generation and ServoHorizontal Deflection

In the preferred embodiment of the invention, the horizontal deflection of the laser swath is provided by the rotating polygon mirror 306. The drive motor 3000 is locked to the incoming horizontal drive signal which is synchronized with the start of each swath (horizontal line group) read out under control of the swath memory banks 922. Optical feedback for a control system is provided by a line 972 scribed on the top of the polygon mirror 306 which aligns with the facet edge. Each facet is used to deflect one swath. Thus, the polygon facet edge signal derived from the scribed line and the incoming horizontal drive serve as inputs to the motor servo phase locked loop 970. Although Figure 8D illustrates a polygon mirror 306 having a total of nine facets, since the polygon mirror only provides horizontal deflection, the number of facets is not critical to the design of the system. In particular, if it takes eight swaths to make a complete 1024 line raster, correspondingly it takes eight facets to make one frame. The vertical blanking time (time for the interlace galvanometer to reset) is accommodated by skipping an integer number of facets.

Vertical Deflection

In the preferred embodiment of the invention, vertical deflection is provided by a galvanometer 304 having an integral mirror 304. The mirror 304 moves in a continuous and linear fashion during each active frame time. Since the mirror 304 cannot move instantaneously or in discrete steps, it therefore moves during the entire active swath time. At the end of a swath and after horizontal blanking, the

-48-

amount of vertical deflection is precisely enough so that line one of swath number two begins underneath the line 128 of swath number one. This causes the raster to tilt by an amount of one swath's height, or 128 scan lines. This tilt is removed optically before the raster is projected on the screen. The amount of time needed for vertical retrace is estimated to be between ten and thirty percent of the total frame time. The sync blanking time of the input video and graphics sources will vary depending on the display standard which they conform to. In any system with a rotating polygon mirror 306 there will always be some discrete number of facets to skip over one frame time and therefore a sync blanking ratio can be established. If the frame time is the same for the input video as it is the scanned out image, then the data can be read out from the frame store slightly slower or faster to meet the integer criteria. As previously described with respect to Figures 1, 4 and 5, a galvanometer may be used to provide the capability to display interlaced formats.

Optical Scanning/Projection System

Figure 12 illustrates a schematic layout of one embodiment 1200 of the optical scanning/projection system 300 of Figure 1. This embodiment 1200 utilizes a conventional rotating polygon mirror 1202 and the galvanometer deflector mirror 302 to raster successive swath scans vertically down the screen. An appropriate prescan collimator 1204, a vertical fold mirror 1206, a horizontal fold mirror 1208 and a 1:1 relay system 1210 are utilized to image the output of the white light laser array source 100 of Figure 1 at the facets of the rotating polygon mirror 1202. The projection optics 312 relay this image to

-49-

the projection screen 400. In the preferred embodiment of the invention, the prescan collimator 1204 is a spherical mirror. The 1:1 relay 1210 consist of a first concentric spherical mirror 1212 and a second spherical mirror 1214, both mirrors being nearly concentric. The mirrors 1212 and 1214 are designed to operate within a finite telocentric field. The functions of the relay 1210 have been inverted to thereby image the collimated field from pupil to pupil with unity magnification.

Referring now to Figure 13, another embodiment 1300 of the optical scanning/projection system 300 of Figure 1 is shown. This embodiment 1300 differs from the embodiment 1200 of Figure 12 in that the rotating right polygon mirror 1202 of Figure 12 is replaced with a tilted facet rotating polygon mirror 1302 and the galvanometer mirror 302 is replaced with a fixed fold mirror 1304. In this embodiment 1300, the successively angled surfaces of the rotating polygon 1302 control the successive swath scans of the output from the white light laser array source 100 of Figure 1. Because of the successively angled surfaces of the rotating polygon mirror 1302, the successive swaths are vertically rastered down the screen 400 thereby eliminating the need for a galvanometer mirror 302 and its integral galvanometer 304 as shown in Figure 1.

In Figure 13 collimated output from the white light laser array source 100 is directed to the polygon mirror 1302 via a pre-scan collimator 1301, the fixed fold mirror 1304, a vertical fold mirror 1305, a polarizing beamsplitter 1306 (used in reflection) a quarter waveplate 1308 and a horizontal fold mirror 1309. The angled polygon 1302 scans 10 swaths of a 128 line array to thereby provide a 1280

-50-

line field. This scan field is split from the input to the polygon mirror 1302 by passing through quarter waveplate 1308 a second time (to complete a 90° rotation of the polarization) and transmitted through the polarizing beam splitter 1306. An F-theta scan lens 1310 creates an intermediate image that is displayed onto the screen 400 by a projection lens 1312. The geometry of the angled polygon mirror 1302 must be selected to balance two competing raster errors. The optical scan angle produced by one facet must be sufficiently small to minimize swath mismatch, but sufficiently large to minimize the raster cross-scan displacement caused by facet pyramidal angle error plus the effects of wobble of the angled polygon mirror.

While the embodiments of the various aspects of the present invention that have been described are the preferred implementation, those skilled in the art will understand that variations thereof may also be possible. Therefore, the invention is entitled to protection within the full scope of the appended claims.

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WE CLAIM

1. An image projection apparatus comprising:
a plurality of primary wavelength light
beam sources for generating a plurality of
5 parallel light beams;
means for modulating each parallel light
beam; and
means for deflecting and projecting the
modulated parallel light beams to generate
10 pixels onto a image receptor.
2. The image projection apparatus of Claim 1
wherein the plurality of primary wavelength light
beam sources comprises:
linear arrays of solid state laser diodes.
- 15 3. The image projection apparatus of Claim 1
wherein the plurality of primary wavelength light
beam sources further comprises:
a red light linear array for generating a
plurality of red light beams;
20 a blue light linear array for generating a
plurality of blue light beams;
a green light linear array for generating a
plurality of green light beams; and
means for combining the red, green and blue
25 beams into parallel light beams.
4. The image projection apparatus of Claim 3
wherein said means for combining further comprises:
a mirror for reflecting one of the red,
green and blue plurality of light beams;
30 a first dichroic filter for reflecting one
of the red, green and blue plurality of light

-52-

beams and transmitting the plurality of light beams reflected by the mirror; and

5 a second dichroic filter for reflecting one of the red, green and blue plurality of light beams and transmitting each plurality of light beams reflected and transmitted by the first dichroic filter.

10 5. The image projection apparatus of Claim 2 wherein said plurality of primary wavelength light beam sources further comprises:

at least one non-linear harmonic converter for multiplying the frequency of light beams generated by the linear array of solid state laser diodes.

15 6. The image projection apparatus of Claim 1 wherein said means for modulating each parallel light beam further comprises:

20 a pulse width modulator operative to vary the amount of energy emitted from each primary wavelength light beam source.

7. The image projection apparatus of Claim 1 wherein:

25 the number of parallel light beams is substantially equal to the number of lines projected across the screen.

8. The image projection apparatus of Claim 1 further comprising:

means for receiving a signal in a first format, the signal containing image data; and

-53-

means for remapping the image data into a second format for modulating each parallel light beam.

5 9. The image projection apparatus of Claim 1 wherein said means for remapping comprises a serial to parallel converter.

10 10. The image projection apparatus of Claim 1 wherein said means for deflecting and projecting further comprises:

 at least one lens; and
 at least one mirror.

15 11. The image projection apparatus of Claim 1 further including:

 a galvanometer operative to deflect the modulated parallel light beams.

20 12. An image projection apparatus comprising:
 a plurality of primary color light beam sources for generating a plurality of parallel white light beams;

 means for modulating each parallel white light beam;

25 means for successively deflecting the modulated parallel white light beams, each successive deflection angled to position successive parallel white beams parallel to and adjacent to the previously deflected parallel white light beam.

13. The image projection system of Claim 12 further including:

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a galvanometer operative to deflect the parallel white light beams to thereby provide at least one offset field within a frame.

- 5 14. A method of projecting an image comprising the steps of:
- providing a plurality of parallel light beams of a first primary color;
- providing a plurality of parallel light beams of a second primary color;
- 10 providing a plurality of parallel light beams at a third primary color;
- modulating each of the plurality of parallel light beams;
- combining the modulated parallel light beams to form a plurality of modulated parallel white light beams;
- 15 deflecting the modulated parallel white light beams; and
- projecting the modulated and deflected white light beams onto an image receptor.
- 20

15. The method of Claim 14 wherein the image receptor comprises:
- an eye.

- 25 16. An image projection apparatus comprising:
- video processing means for receiving image data and generating digital red signals, digital green signals, digital blue signals and timing signals representative of image data;
- a first laser array for generating a plurality of parallel beams of a first primary color;
- 30

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a second laser array for generating a plurality of parallel beams of a second primary color;

5 a third laser array for generating a plurality of parallel beams of a third primary color;

10 means for combining the plurality of parallel beams of the first, second and third primary colors to generate a plurality of parallel white light beams;

means for modulating the intensity of each of the beams of the first, second and third primary colors in response to the digital signals; and

15 means for deflecting and projecting the plurality of parallel white light beams across a screen.

17. An image projection apparatus comprising:
20 video processing means for receiving image data and generating digital luminance signals and timing signals representative of image data;
a laser array for generating a plurality of parallel light beams;

25 means for modulating the intensity of each of the beams in response to the luminance signals; and

means for deflecting and projecting the plurality of parallel light beams onto an image receptor.

30 18. A method of forming an image on a screen comprising the steps of:

receiving image data representative of an image;

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projecting a first plurality of parallel
light beams across a first area of a screen;
projecting a second plurality of parallel
light beams across a second area of the screen;
5 and
modulating the first and second plurality
of parallel light beams with the image data.

19. A method of deflecting a plurality of
parallel light beams across a screen comprising the
10 steps of:

reflecting a plurality of parallel light
beams from a first rotating reflective surface;
and

15 reflecting the plurality of parallel light
beams from a second rotating reflective surface,
the second rotating reflective surface adjacent
to and skewed from the first rotating reflective
surface.

20. An apparatus for generating a plurality of
20 parallel beams of light comprising:

a linear array of light emitting diodes
mounted on a base, operative to emit parallel
beams of light;

25 a lens mounted on the base operative to
collimate the parallel beams of light.

21. An apparatus for generating parallel beams
of white light comprising:

30 a first array for generating a first
plurality of parallel beams of a first primary
color;

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a second array for generating a second plurality of parallel beams of a second primary color;

5 a third array for generating a third plurality of parallel beams of a third primary color;

a mirror for reflecting the first plurality of parallel beams;

10 a first dichroic filter for combining the first plurality of parallel beams with the second plurality of parallel beams; and

a second dichroic filter for combining the third plurality of parallel beams with the combined pluralities of first and second parallel beams.

15

22. An image projection apparatus comprising:

converter means for outputting a red digital signal, a green digital signal and a blue digital signal in response to a video signal representative of a color image;

20

time base correction means for correcting timing errors of the red, green and blue digital signals;

serial to parallel conversion means for generating sets of parallel data representative of the color image;

25

a plurality of primary wavelength light beam sources for generating a plurality of parallel beams;

30 modulation means for modulating the plurality of the parallel beams in response to the parallel data; and

means for deflecting and projecting the modulated parallel beams.

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23. The image projection apparatus of Claim 22 further comprising:

5 image resizing means for remapping the red, green and blue digital signals from a first format to a second format.

24. The image projection apparatus of Claim 22 further comprising:

 a reflective screen.

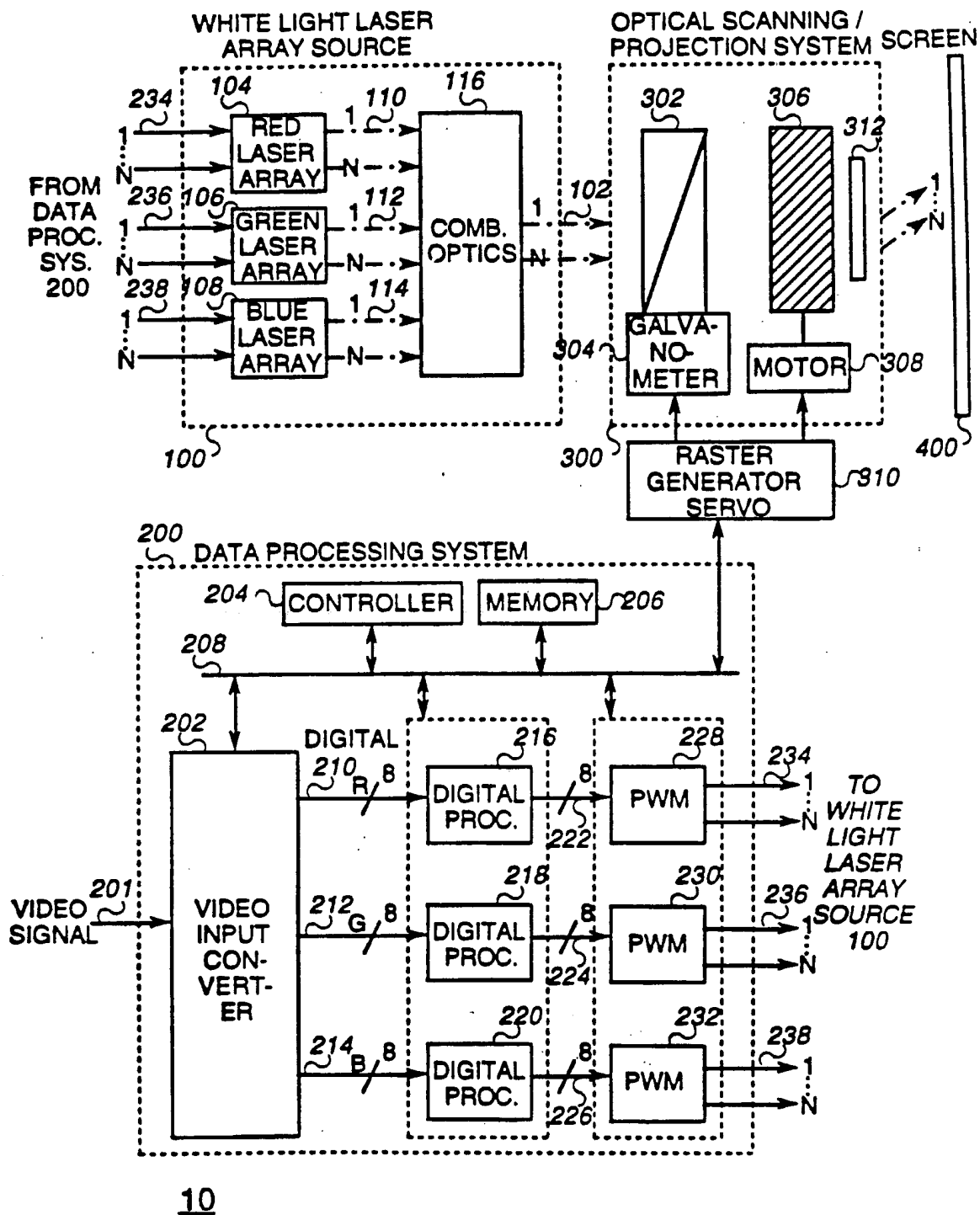
25. The image projection apparatus of Claim 22 wherein the modulation means further comprises:

10 a pulse width modulator for generating pulses having a total energy content corresponding to the luminance of at least the one pixel of the color image.

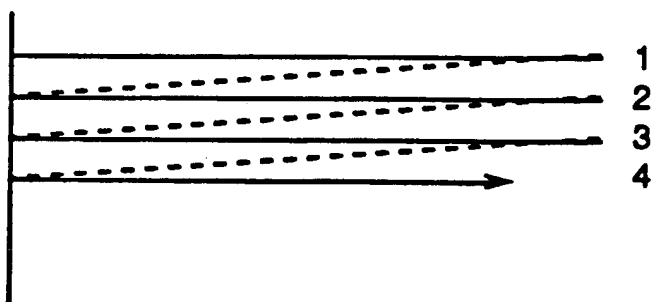
26. The image projection apparatus of Claim 22 wherein the modulation means further comprises:

15 at least one Mach-Zehnder interferometer.

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STANDARD CRT RASTER SCAN

FIG. 2A

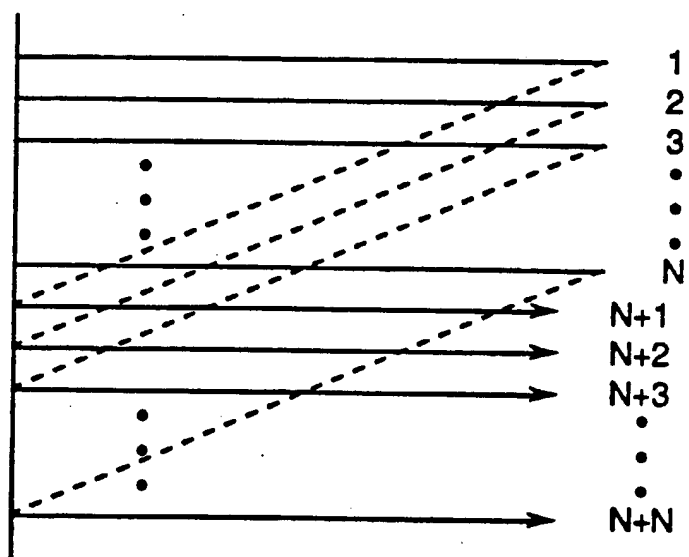


FIG. 2B

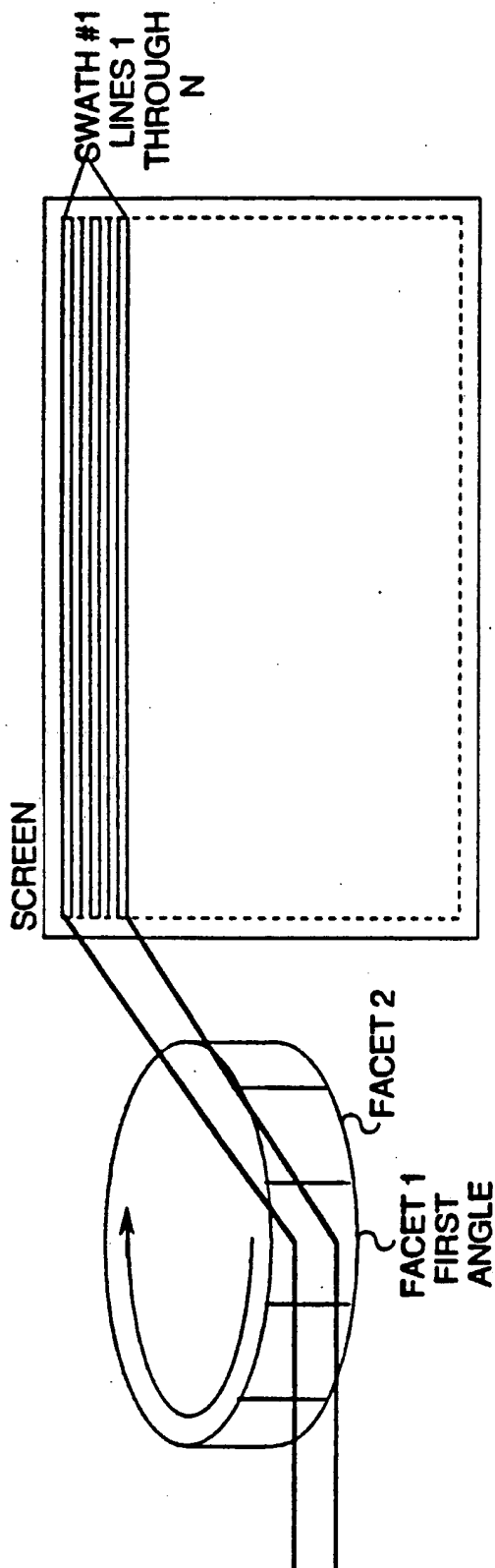


FIG. 3A

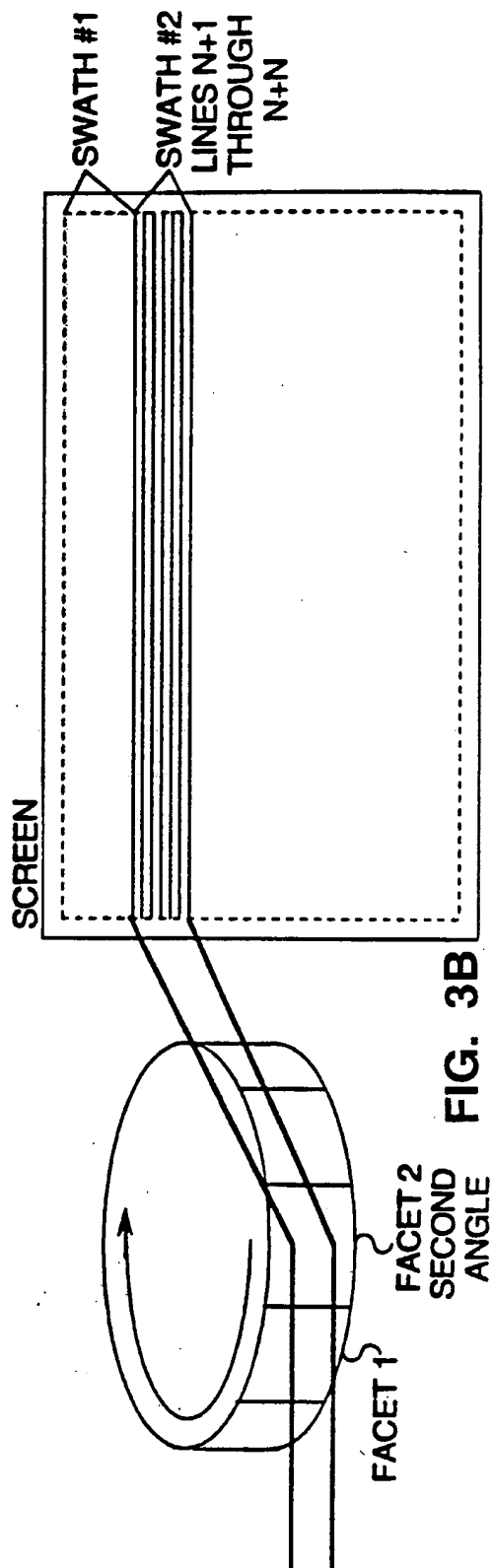


FIG. 3B

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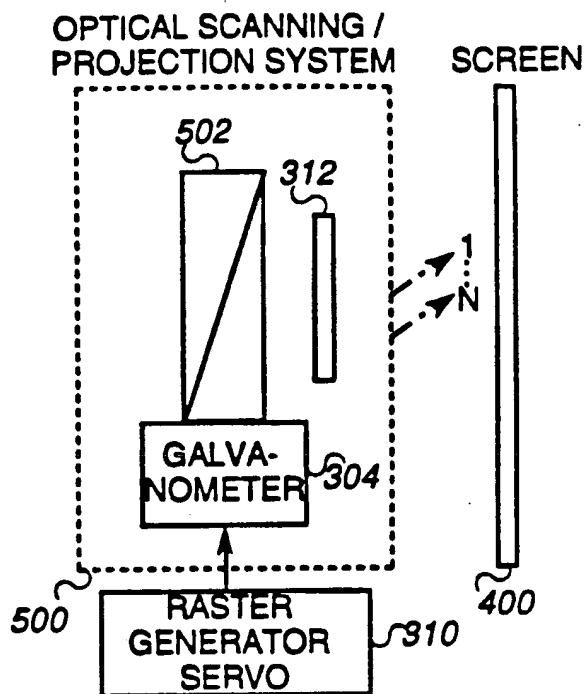


FIG. 4

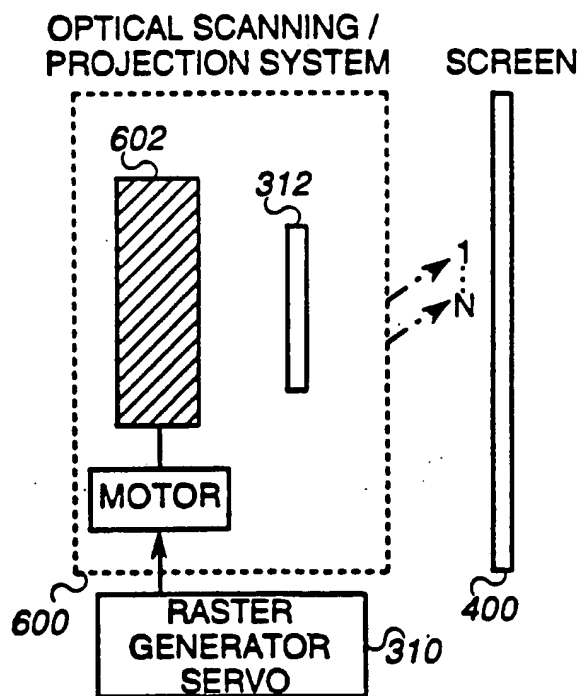
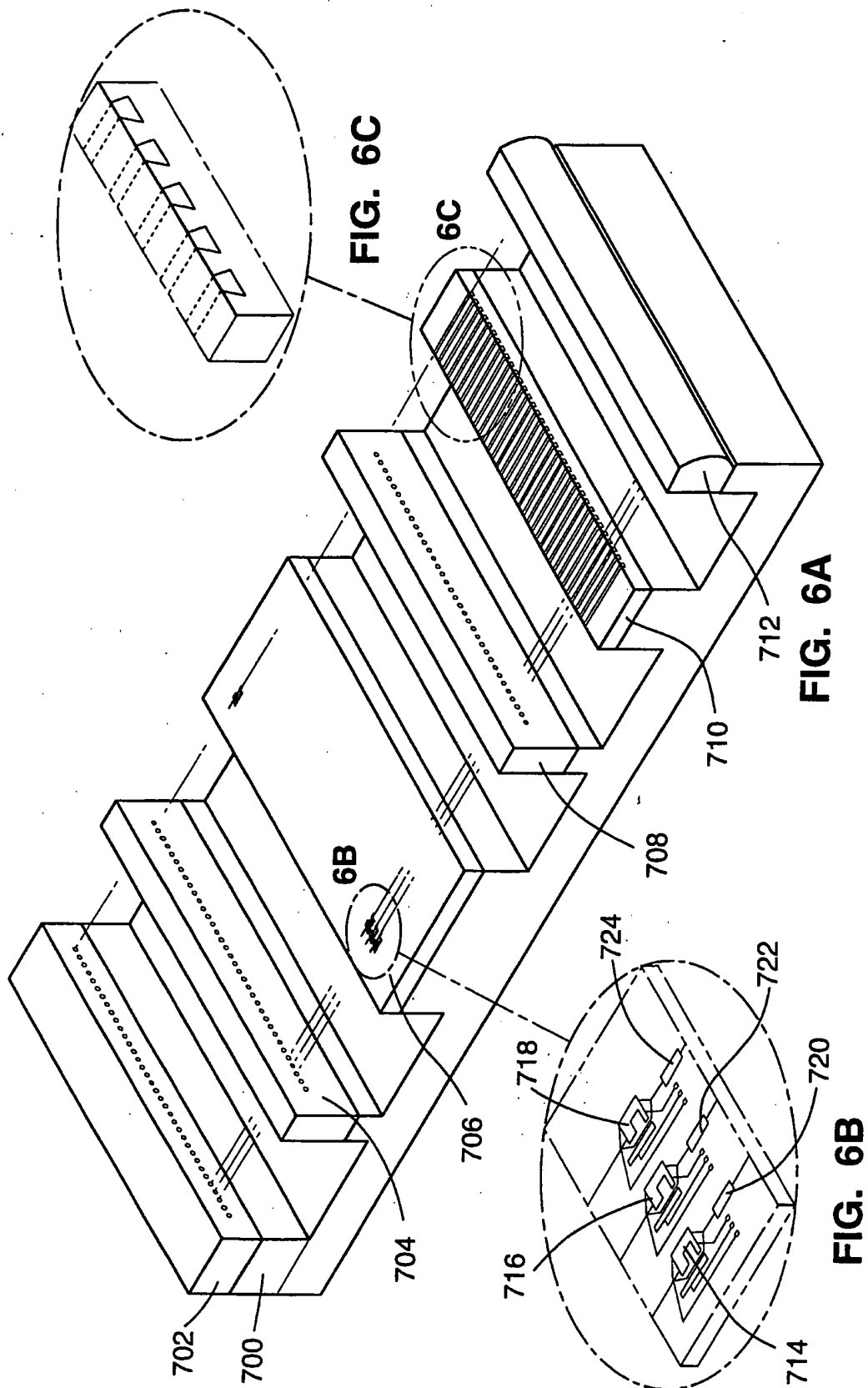


FIG. 5

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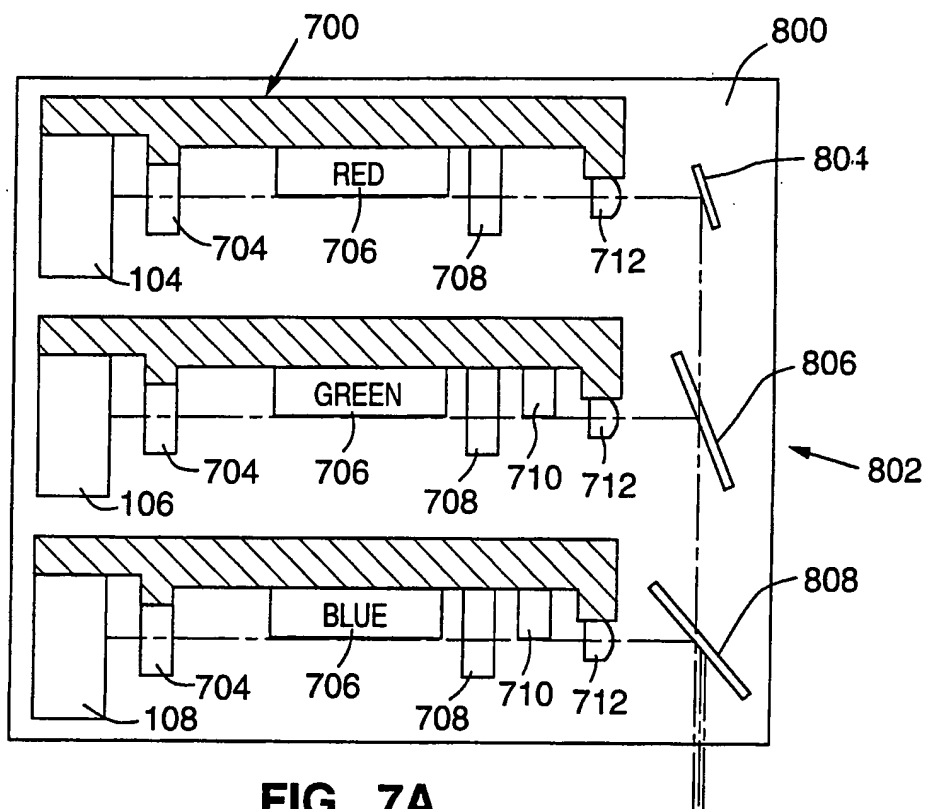
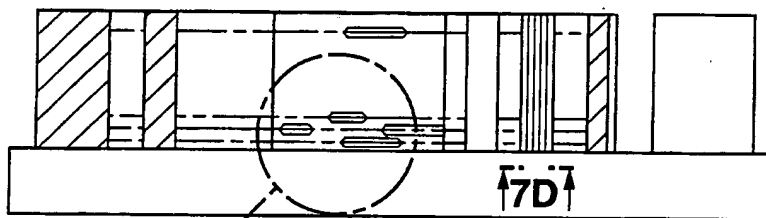


FIG. 7A

100



7C

FIG. 7B

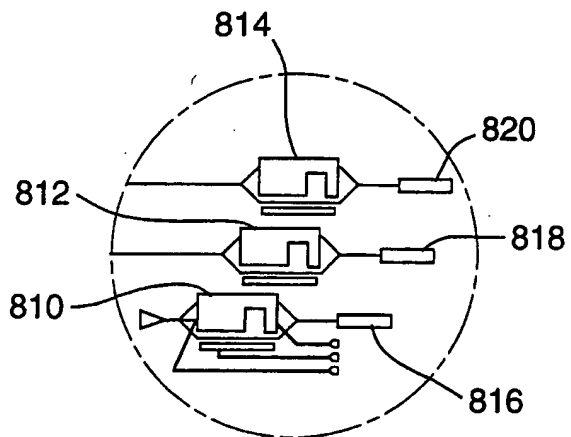


FIG. 7C

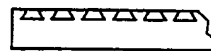


FIG. 7D

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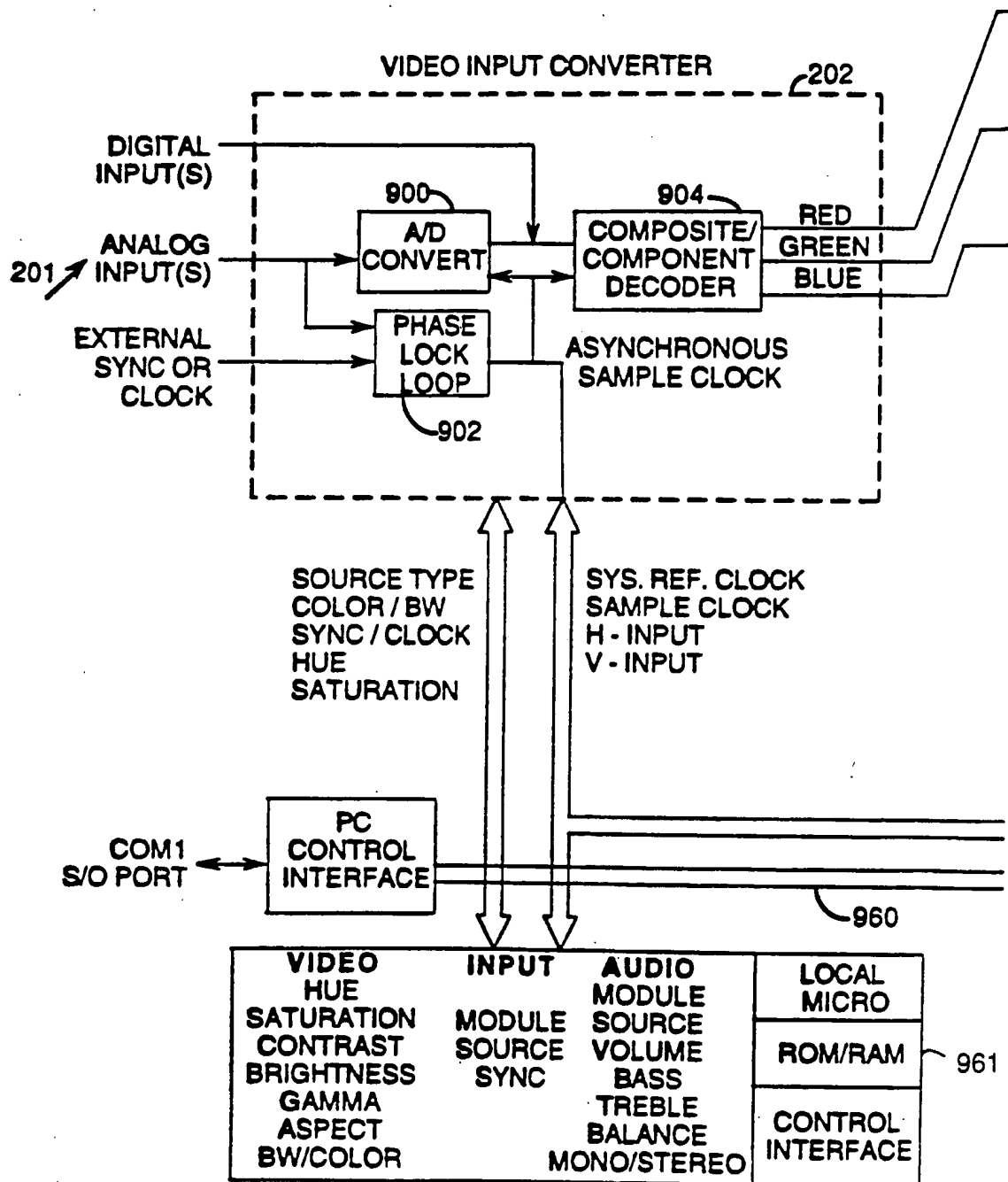
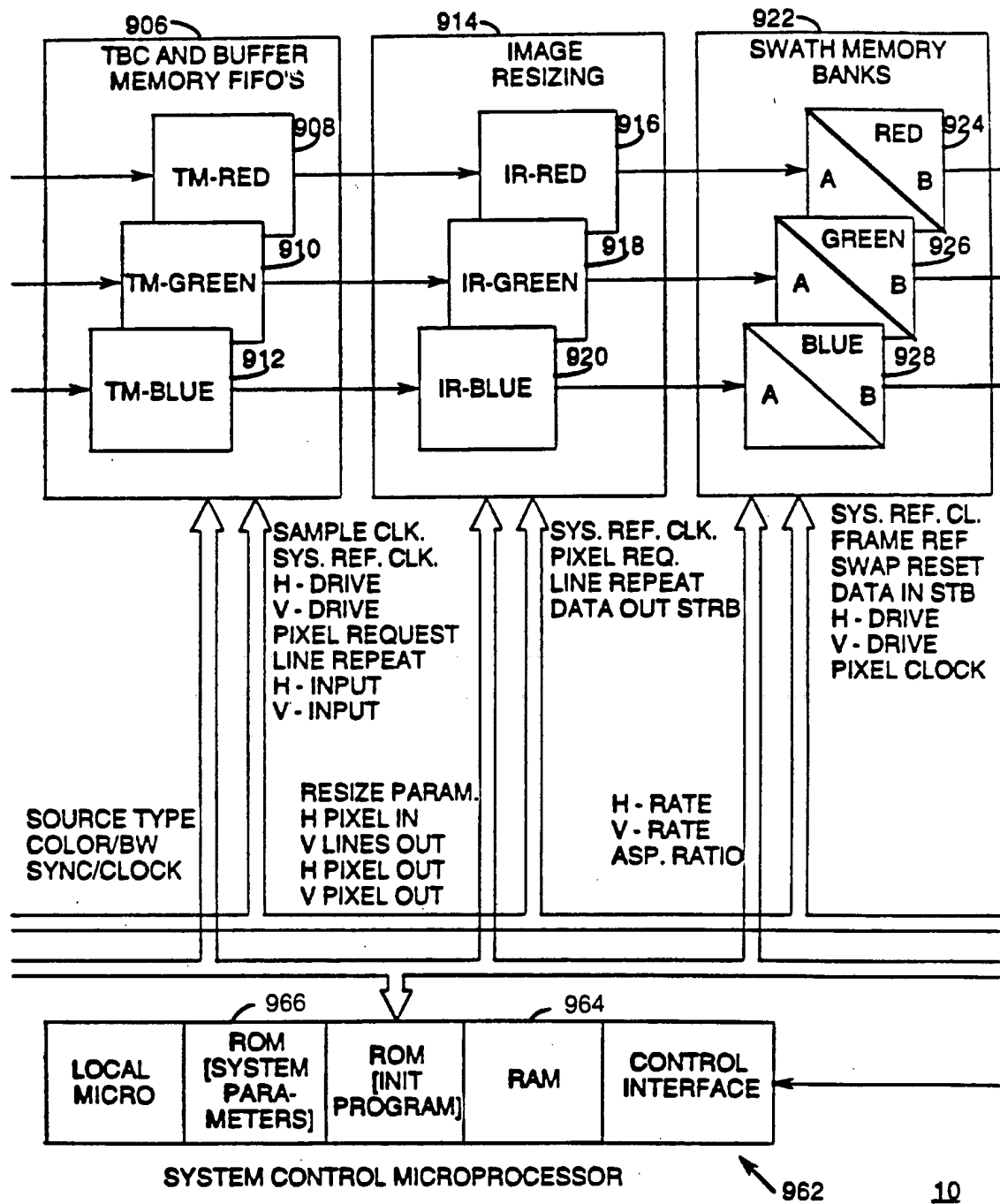


FIG. 8A

TO FIGURE 8B →

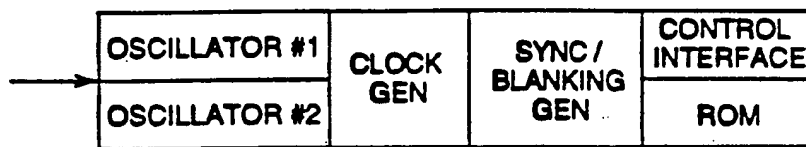
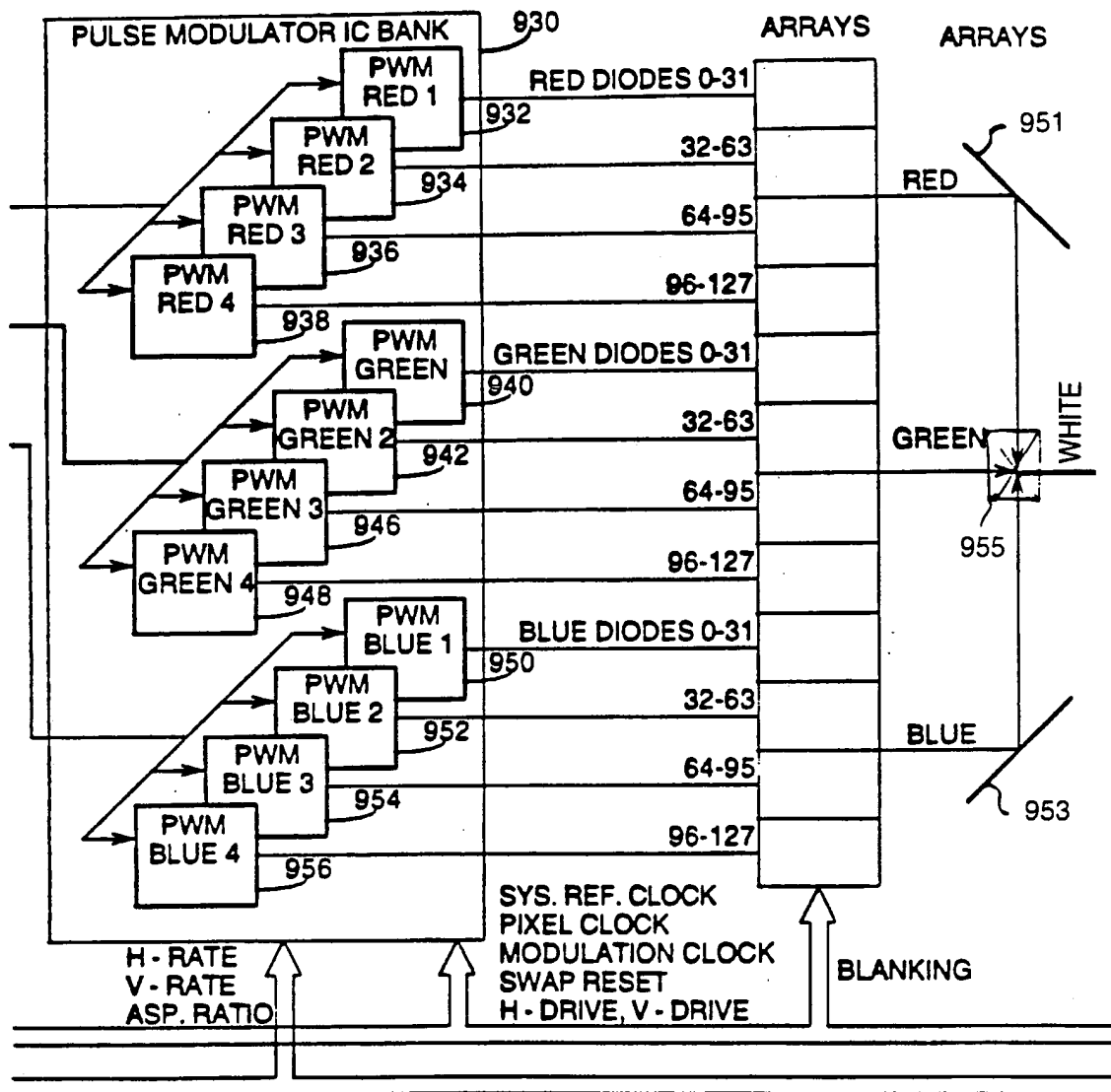
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**FIG. 8B**

← FROM FIGURE 8A

TO FIGURE 8C →

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SYSTEM TIMING GENERATOR 968

← FROM FIGURE 8B

TO FIGURE 8D →

FIG. 8C

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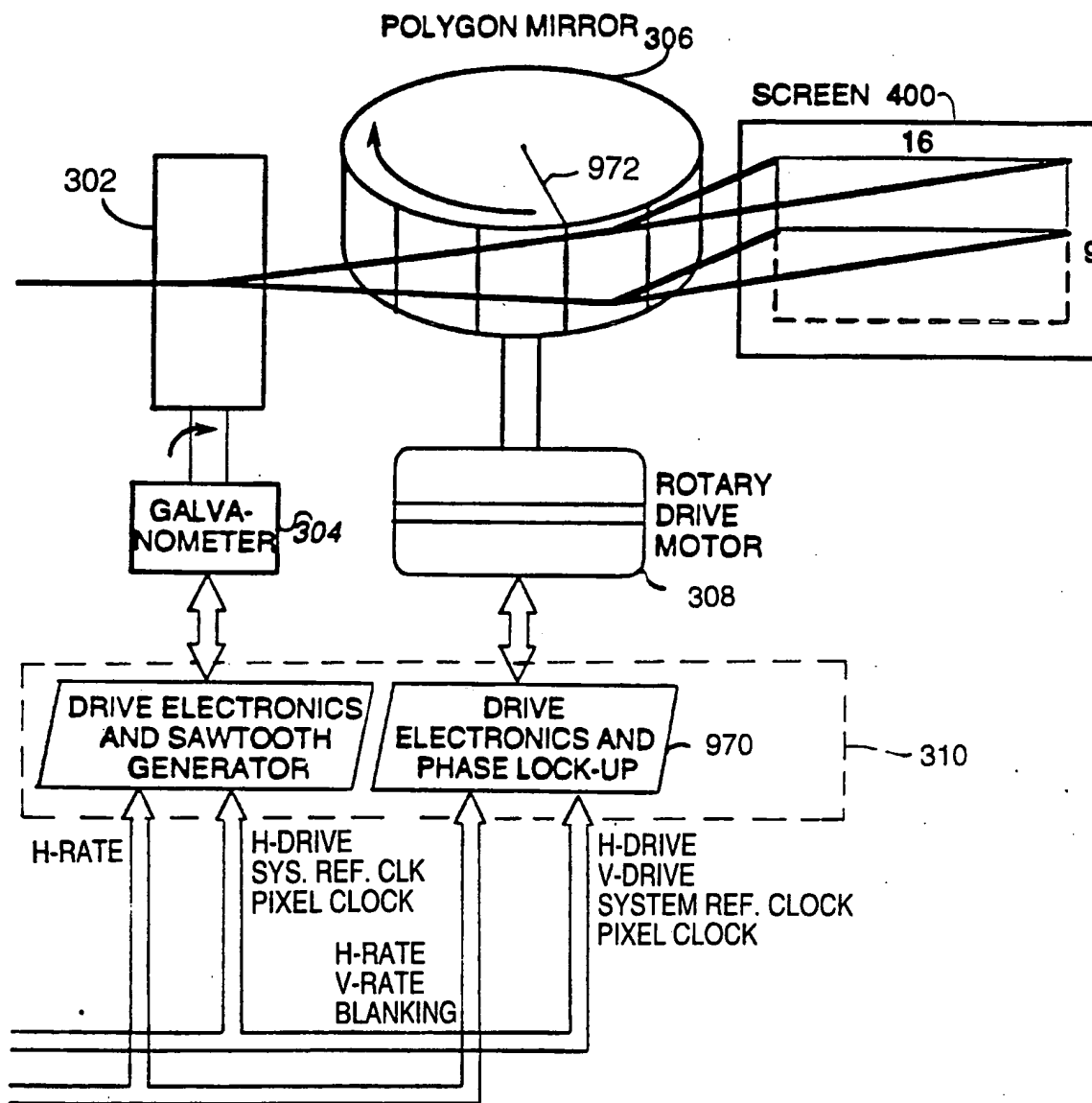


FIG. 8D

← FROM FIGURE 8C

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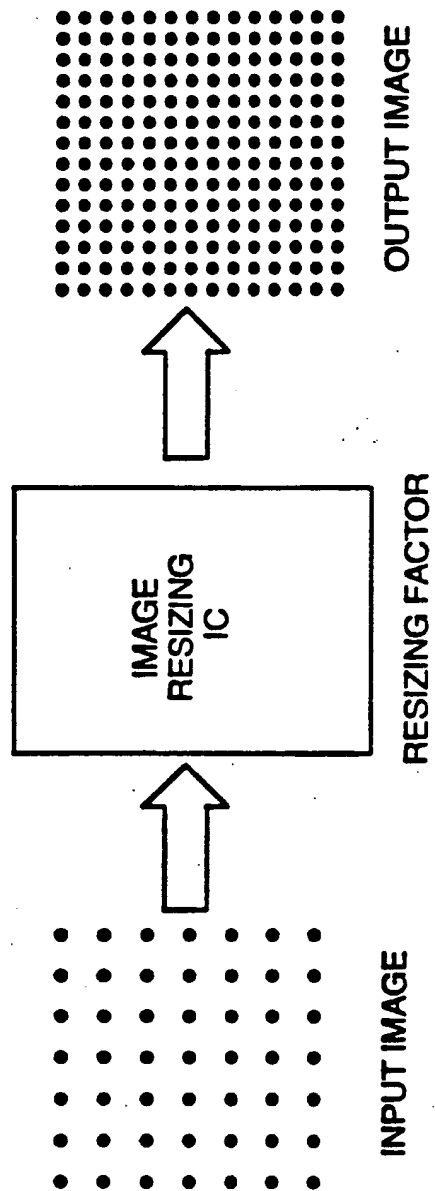


FIG. 9

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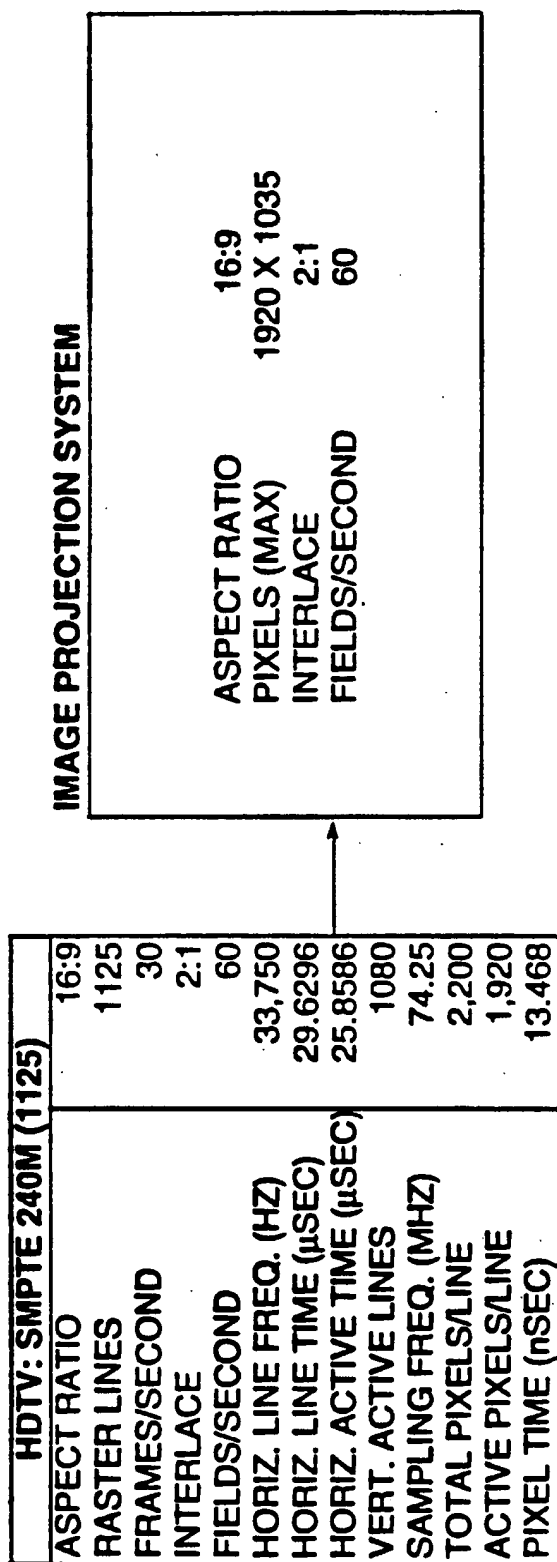


FIG. 10A

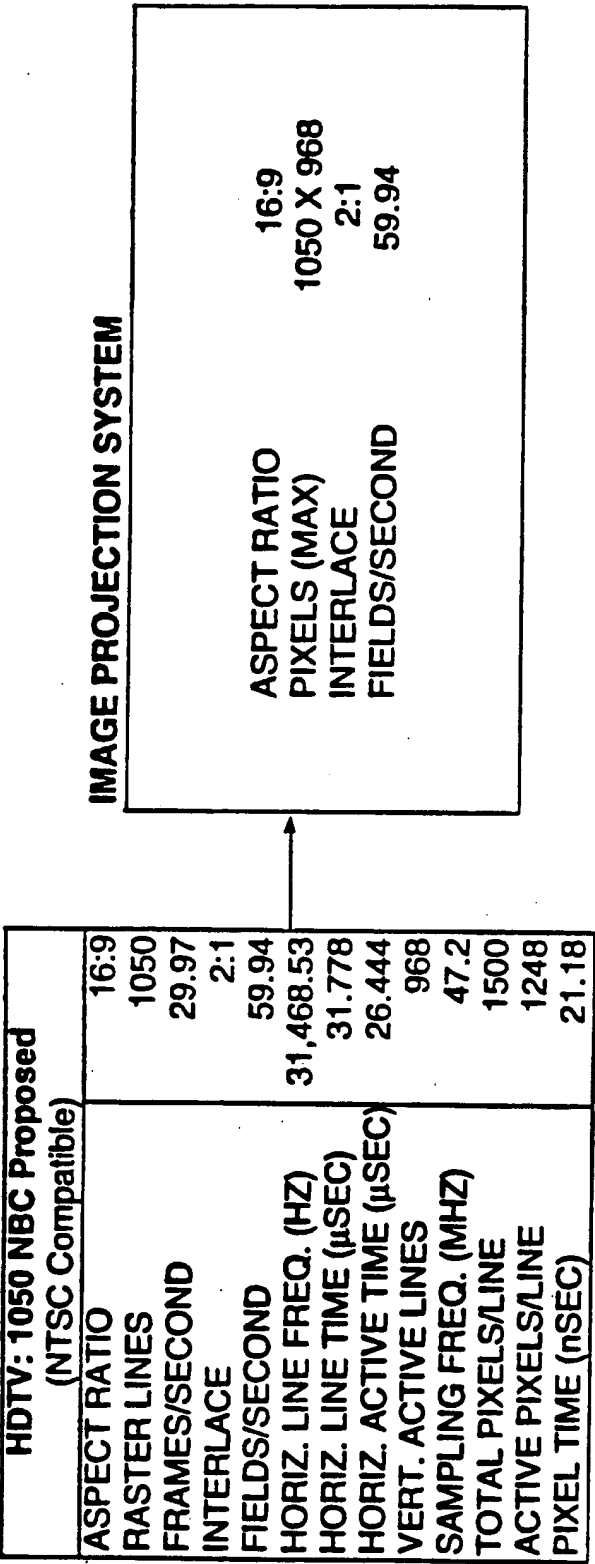


FIG. 10B

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NTSC: RS-170A	
ASPECT RATIO	4:3
RASTER LINES	525
FRAMES/SECOND	29.97
INTERLACE	2:1
FIELDS/SECOND	59.94
HORIZ. LINE FREQ. (HZ)	15,734.26
HORIZ. LINE TIME (μ SEC)	63.556
HORIZ. ACTIVE TIME (μ SEC)	52.5
VERT. ACTIVE LINES	484
SUBCARRIER FREQ. (MHZ)	3.57955
SAMPLING FREQ. (MHZ)	14.318
TOTAL PIXELS/LINE	910
ACTIVE PIXELS/LINE	768
PIXEL TIME (nSEC)	69.841

IMAGE PROJECTION SYSTEM	
ASPECT RATIO	4:3
PIXELS (MAX)	525 X 484
INTERLACE	2:1
FIELDS/SECOND	59.94

FIG. 10C

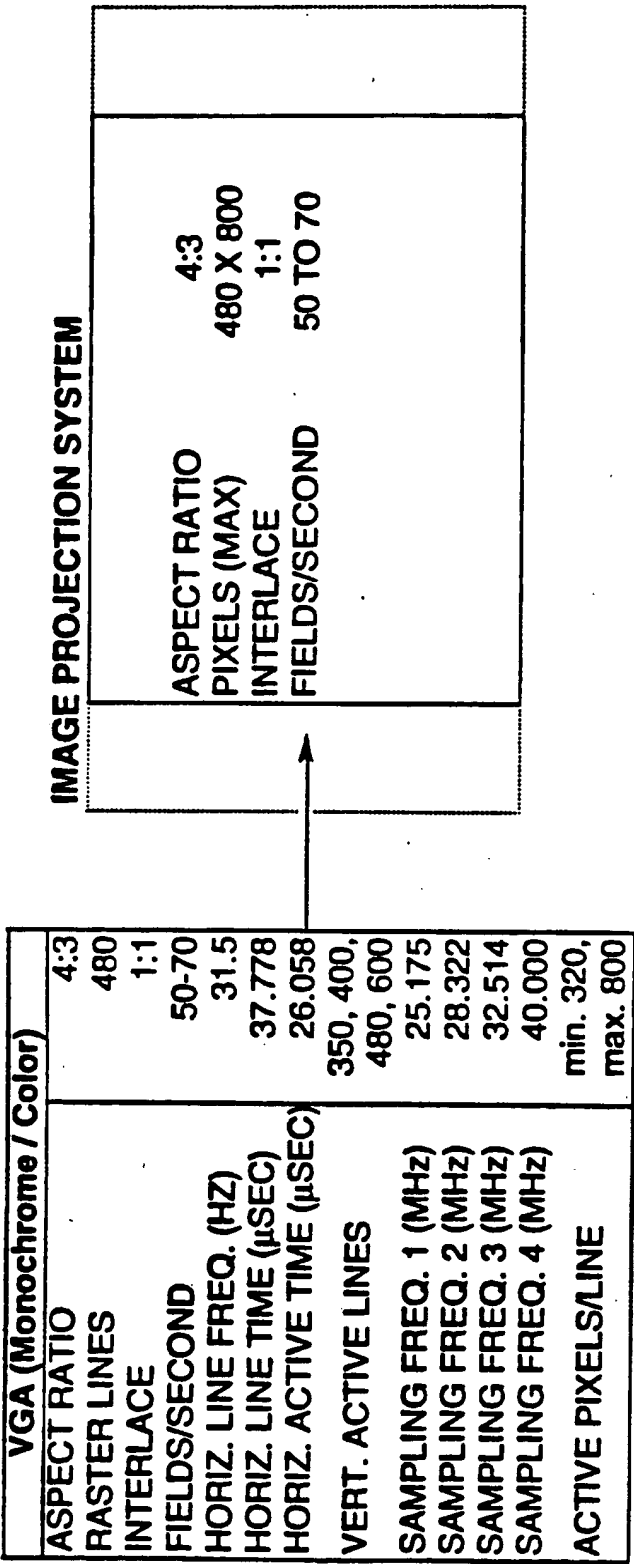
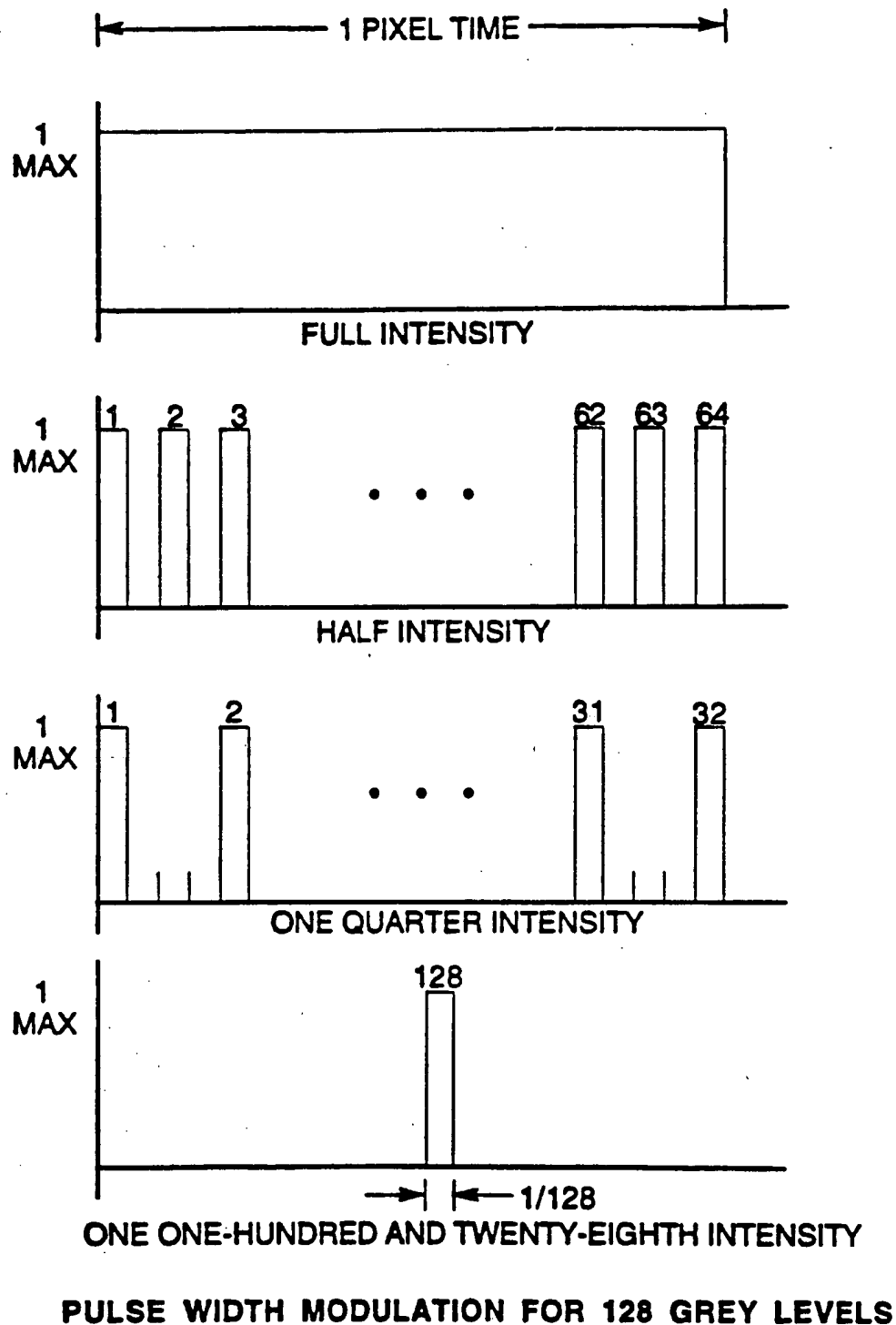


FIG. 10D

**FIG. 11**

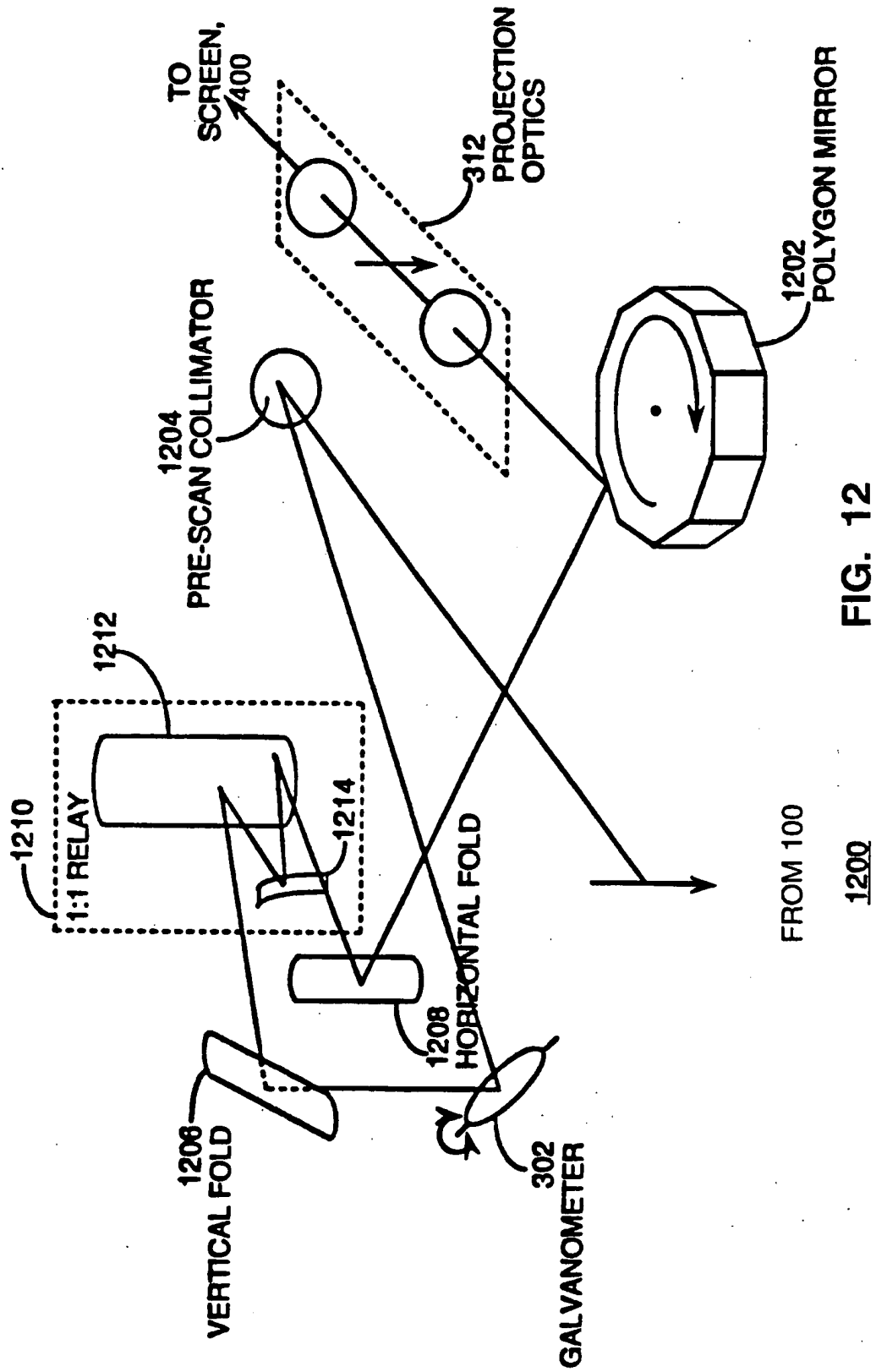


FIG. 12

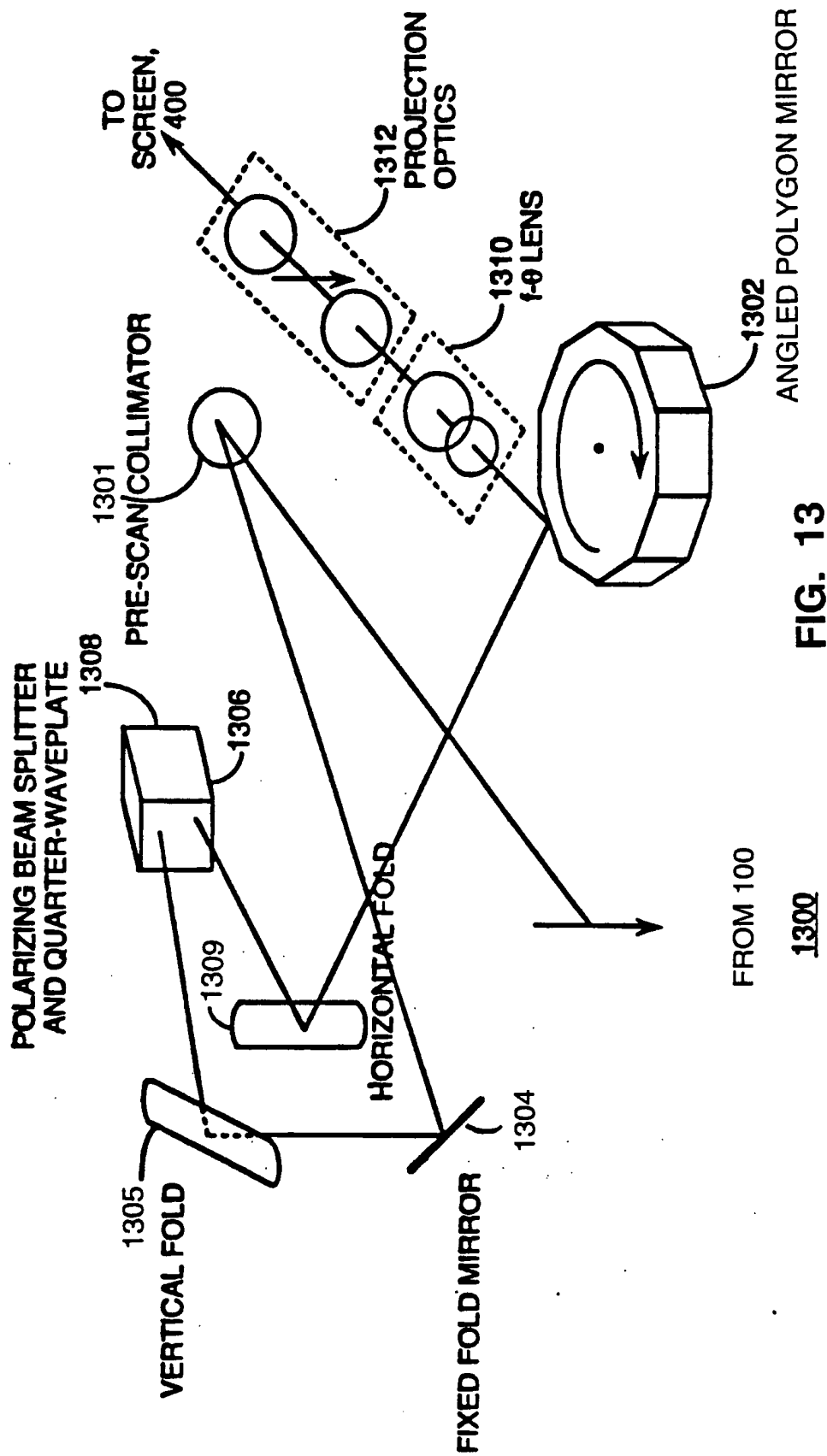


FIG. 13

FROM 100

1300

Internat'l Application No
PCT/US 94/01143

According to International Patent Classification (IPC) or to both national classification and IPC

Minimum documentation searched (classification system followed by classification symbols)
IPC 5 H04N G02B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	GB,A,2 252 472 (SAMSUNG ELECTRONICS CO LIMITED) 5 August 1992 see abstract; claims 1-4,6; figures 1-3 see page 4, line 2 - line 8 see page 5, line 1 - line 5 see page 6, line 6 - line 13 see page 6, line 24 - page 7, line 10	1,2,6, 10,17,19
Y		3,4,12, 14,16,21
A		5,9,18
X	--- PATENT ABSTRACTS OF JAPAN vol. 7, no. 262 (P-238) 22 November 1983 & JP,A,58 145 914 (CANON K.K.) see abstract	1
A	---	7
	--- -/--	

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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- "T"** later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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- "&"** document member of the same patent family

Date of the actual completion of the international search

31 May 1994

Date of mailing of the international search report

13. 07. 94

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Authorized officer

Van Doremaalen, J

INTERNATIONAL SEARCH REPORT

 Interna Application No
 PCT/US 94/01143

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 9, no. 6 (P-326) 11 January 1985 & JP,A,59 155 826 (HITACHI SEISAKUSHO K.K.) see abstract	19
Y	---	11,13
X	US,A,4 297 723 (C.M.WHITBY) 27 October 1981	1,8,10, 12,18, 22-24
Y	---	3,4, 11-14, 16,21
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